



samer22 fadytvsat

LCD TV

SCHEMATICS and EXPLODED VIEW

CHASSIS : LA12E

MODEL : 42LV5500 42LV5500-UA

CAUTION

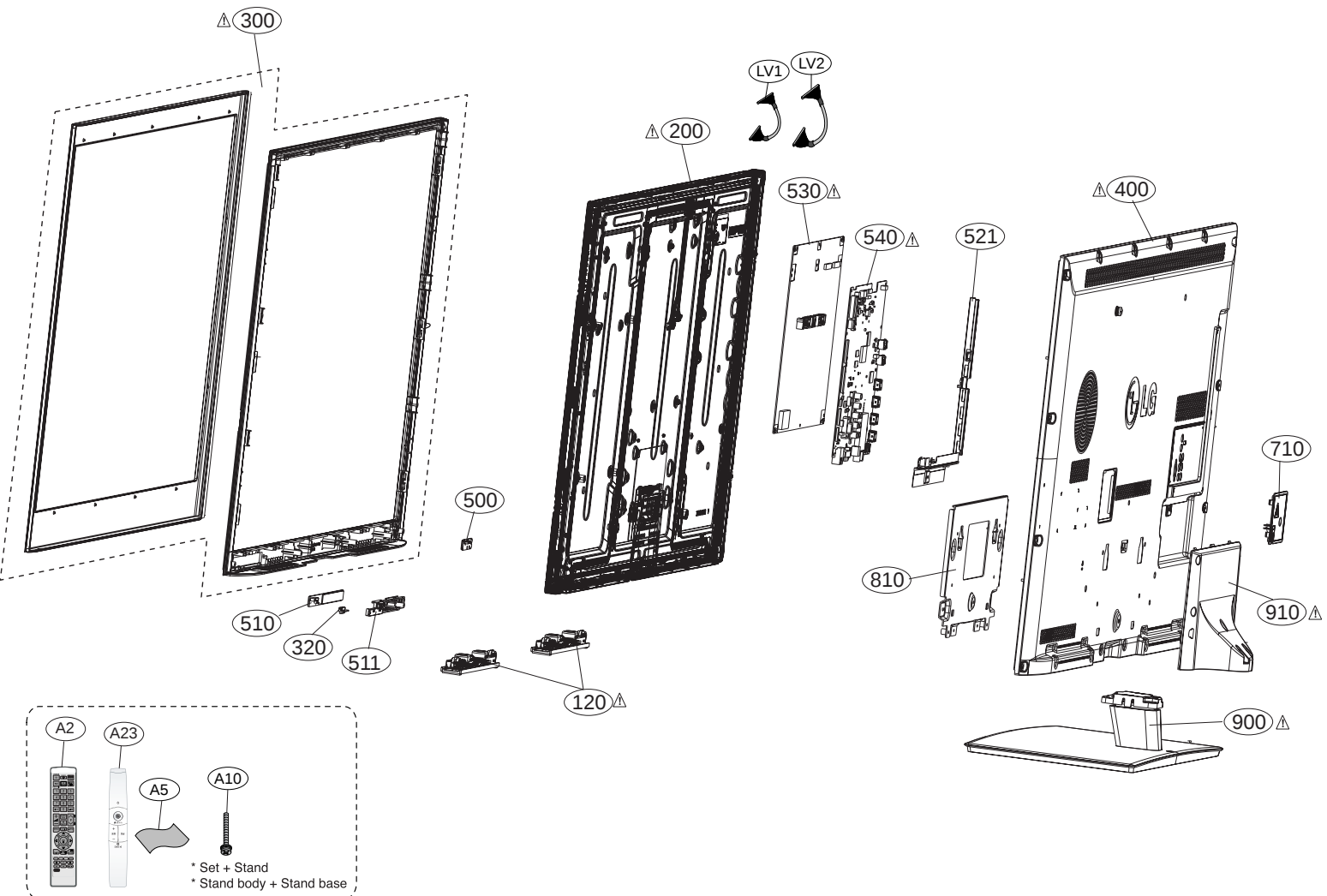
BEFORE SERVICING THE CHASSIS,
READ THE SAFETY PRECAUTIONS IN THIS MANUAL.

Printed in Korea

EXPLODED VIEW

IMPORTANT SAFETY NOTICE

Many electrical and mechanical parts in this chassis have special safety-related characteristics. These parts are identified by Δ in the Schematic Diagram and EXPLODED VIEW. It is essential that these special safety parts should be replaced with the same components as recommended in this manual to prevent X-RADIATION, Shock, Fire, or other Hazards. Do not modify the original design without permission of manufacturer.



42LV5500 EXPLODED VIEW PARTS LIST

ITEM	PART No	DESCRIPTION
120	EAB62088401	Speaker EN1080C-6795 ND 10W 80HM 79DB 150HZ 200x25x27
200	EAJ61749101	Panel LC420EUF-SDA1 FHD 42.0INCH 1920X1080 400CD Color 16/9 120Hz with T-CON 10bit(D) Edge 112PKG 10000K (80.2W) LG Display Co. Ltd.
300	ABJ73288707	Ft Frame 42LV5500- . 42" 42LV5500 RS Local BOM
320	MES62378302	MOLD PC EYE 47LV5500 PC 7 PHY 47LW IR Indicator (for C/SKD)
400	ACQ83852512	42LV5500 . 42" 42LV5500 RS Local Back Cover assy
500	EBR72499601	MOTION REMOCON RECEIVER + 3D EMMITER ASSY BARUN ELECTRONICS CO., LTD.
510	EBR72671001	IR+TOUCH ASS'Y CONTROL T.T . LV/LW5500 . LV/LW5500 IR+TOUCH ASS'Y
511	MAZ62829502	MOLD ABS HF380 BRACKET 47LV5500 - ABS, HF-380 Touch PCB Bracket (for C/SKD)
521	MGJ62689302	PRESS EGI 0.5 SHIELD EGI LV5 Side AV Metal Bracket(NTSC)_CSKD of "01"
530	EAY62169801	SMPS LGP4247-11SLPB FREE LGP4247-11SLPB LCD HNE/YUYANG/LGIT/LIENCHANG - H&E CO.,LTD
540	EBR71850702	MAIN M.I LA12E LV5500-UA CKD - (AUSYLHR)
540	EBR73146501	MAIN PAKING LA12E 42LV5500-UA ALL For SKD - (AUSYLJR)
540	EBR71850705	MAIN M.I LA12E LV5500-UA CKD - (AUSYLUR)
710	MAZ62827604	MOLD ABS AF-312 BRACKET 47LV7500 - ABS Cord BRK_Non Flame_C/SKD
800	AGU73528803	SHEET C/SKD of 01 (Insulation assy to attach on module for 42/47)
810	MJH62198002	PRESS H-GI 1.6 GUIDE METAL 47LV7700_Stand guide metal_CSKD
900	AAN73430406	STAND 42/47LV370 - HB, STAND_ASS'Y RS Phantom
910	MJH62197902	MOLD LUPOY Supporter PC 47LV7700_Stand body top_PC + GF 20% BK, CSKD
A1	MFL62882745	PRINTING USER LA01A LG GP3 EN (1) LGEUS
A2	AKB72914042	REMOTE CONTROL LCD LS-Con (LV5500) BB TV for N.AMERICA &CANADA &MEXICO
A3	EAD60817902	AC Power Cord 1.625M 75MM 125V 10A SVT 18AWG 3C BLACK. UL/CSA
A5	MAF60779001	COMPLEX ACCESORY 150*150 0.41t cloth cleaner
A7	EAT60713304	WN8522B2-LF-19 Dongle Only AN-WF100 BCM4323 802.11a/b/g/n
A10	FAB30016104	FAB30016104 MACHINE TYPE D4.0 L12.0 WASHER D8.5 t0.6 SEUNGBO MECHA TECH CO.,LTD
A21	MCK66265801	MOLD ABS XG-568 M3 Remocon ABS M3 Remocon Battery Cover
A23	AKB73295502	MOTION M3_REMOTE CONTROLLER(BAR TYPE) FOR U.S.A(LG)
LV1	EAD61652505	LVDS 455MM 0.50MM 41P WHITE UL20861 Reverse A: 95mm B: 220mm C: 160mm D: 55mm
LV2	EAD61668615	LVDS 365MM 0.50MM 51P WHITE UL20861 reverse Locking A: 95mm B: 180mm C: 125mm D: 55mm

IC102
TC580V6350ETA00

NAND_8Gbit

NC_1 1
NC_2 2
NC_3 3
NC_4 4
NC_5 5
NC_6 6
NC_7 7
NC_8 8
NC_9 9
NC_10 10
NC_11 11
NC_12 12
NC_13 13
NC_14 14
NC_15 15
NC_16 16
NC_17 17
NC_18 18
NC_19 19
NC_20 20
NC_21 21
NC_22 22
NC_23 23
NC_24 24
NC_25 25

NC_28 48
NC_27 47
NC_26 46
NC_25 45
I/O8 44
I/O7 43
I/O6 42
I/O5 41
NC_24 40
PSL 39
NC_23 38
VCC_2 37
VSS_2 36
NC_22 35
NC_21 34
NC_20 33
I/O4 32
I/O3 31
I/O2 30
I/O1 29
NC_19 28
NC_18 27
NC_17 26
NC_16 25

R102 7K
R149 16Gbit
R148 16Gbit
R151 16Gbit
C102 4700pF
C101 0.1uF
C104 10uF
C103 0.1uF

NAND_Rbb
NAND_Rbb
NAND_Ceb
NAND_Ceb2
NAND_Cle
NAND_Ale
NAND_Web
FLASH_MP

NAND_DATA[7]
NAND_DATA[6]
NAND_DATA[5]
NAND_DATA[4]
NAND_DATA[3]
NAND_DATA[2]
NAND_DATA[1]
NAND_DATA[0]

+3.3V_Normal
GND

Write Protection
High : Normal Operation
Low : Write Protection

IC102 - *1 TH58DVG450ETA20		
NC_1	1	NC_26
NC_2	2	NC_25
NC_3	3	NC_24
NC_4	4	NC_23
NC_5	5	I/O8
RY/BY2	6	I/O7
RY/BY1	7	I/O6
RE	8	I/O5
CE1	9	NC_22
CE2	10	PSL
NC_6	11	NC_21
VCC_1	12	VCC_2
VSS_1	13	VSS_2
NC_7	14	NC_20
NC_8	15	NC_19
CLE	16	NC_18
ALE	17	I/O4
WE	18	I/O3
MP	19	I/O2
NC_9	20	I/O1
NC_10	21	NC_17
NC_11	22	NC_16
NC_12	23	NC_15
NC_13	24	NC_14
DEV_NAND_16Gbit		

The schematic shows two NAND ECC circuit blocks connected to a +3.3V_Normal supply. The top block is for FA3, FA2, and FALE, featuring resistors R113, R117, R122, R127, R114, R118, R123, and R128, all labeled as 10K OPT. It has four output pins: CI_ADDR[4], NAND_DATA[7], NAND_DATA[2], and NAND_DATA[1]. The bottom block is for NAND ECC (FA3, FA2, FALE), featuring resistors R111, R115, R119, R112, R116, and R120, also labeled as 10K OPT. It has three output pins: CI_ADDR[3], CI_ADDR[2], and NAND_ALE. To the right of the schematics is a list of memory specifications:

```

0000: ST Micro M25P or compatible Serial Flash
0010: 8-bit 512Mbit 512B page SLC NAND Flash devices
0100: 8-bit 128, 256Mbit 512B page SLC NAND Flash devices
0110: 8-bit 1Gbit 2KB page SLC NAND Flash devices
0120: 8-bit 2Gbit, 4Gbit, 8Gbit 2KB page SLC NAND Flash devices
1010: 8-bit 16Gbit, 32Gbit 4KB page SLC NAND Flash devices (O)
0001: 8-bit 8/16/32Gbit 2KB page MLC NAND Flash devices
J0011: 8-bit 16/32Gbit 4KB page MLC NAND Flash devices
0101: 8-bit 32Gbit 8KB page MLC NAND Flash devices
0111: 3B dual IO Serial Flash
1001: 8B dual IO Serial Flash
1011: Fast Serial Flash > 50MHz
1100: OneNAND Flash (always 16-bit)
1110: Reserved
1101, 1111: Reserved

```

NAND_DATA[0]:

- 0: System is LITTLE endian (0)
- 1: System is BIG endian

CI_ADDR[7]:

- 0: Disable EDID automatic Downloading from Flash (0)
- 1: Enable EDID automatic Downloading from Flash

NAND_DATA[6] :

- 0: Disable OSC clock output on chip Pin (0)
- 1: Enable OSC clock output on chip pin.

CI_ADDR[8]:

- 0: Host MIPS run at 500 MHz (0)
- 1: Host MIPS run at 250 MHz

NAND_CLE:

- 0: Differential Oscillators TVM not bypassed (0)
- 1: Differential Oscillators TVM bypassed

NAND_DATA[4]:

- 0: 27MHz TVM Crystal Frequency
- 1: 54MHz TVM Crystal Frequency (0)

CI_ADDR[9], CI_ADDR[11], CI_ADDR[12], CI_ADDR[13]

TVM Crystal oscillator bias/gain control

0000: 210uA
 0001: 390uA
 0010: 570uA
 0011: 750uA
 0100: 930uA (0)
 0111: 1290uA
 1000: 1416uA
 1111: 2190uA
 0101, 0110, 1001, 1010, 1011, 1100, 1101, 1110: Reserved

CI_ADDR[8]:

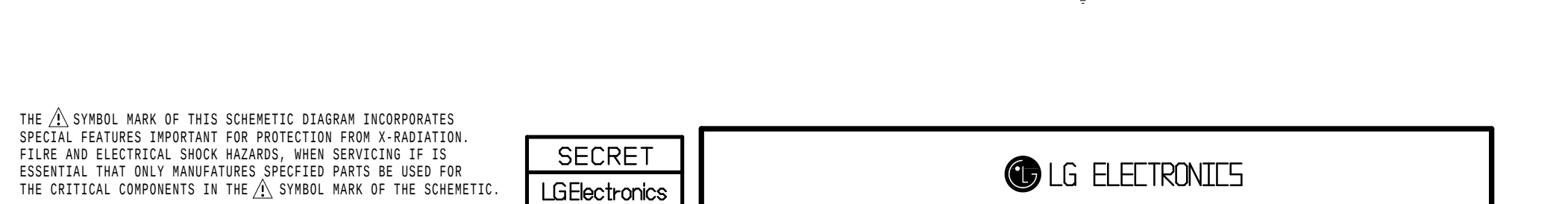
- 0: RESETOUTB (in On/Off only) stay asserted until software releases them.
- 1: Fix amount of delay for de-assertion on RESETOUTB (in On/IOff only) at end of RESETB pulse (0)

NAND_DATA[3]:

- 0: MIPS will boot from external flash (0)
- 1: MIPS will boot from ROM

NAND_DATA[5]:

- 0: FLASH MODE (0)
- 1: BSC_SLAVE(BBS) MODE



IC103
M24AG1-HRMN6TP

A8'h

Write Protection
- Low : Normal Operation
- High : Write Protection

Figure 1: Typical application circuit of the X101 series.

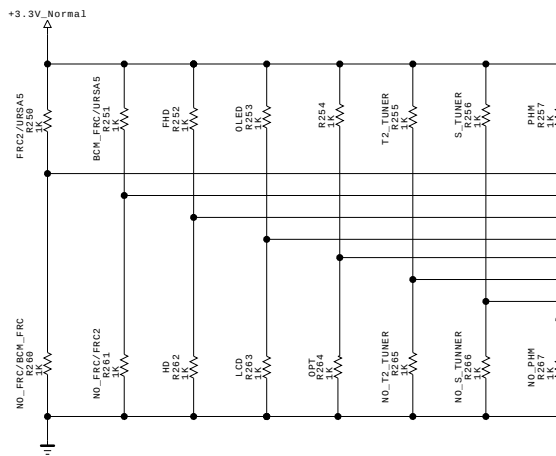
The diagram illustrates two typical application circuits for the X101 series crystal. Both circuits use a 54MHz crystal (X101) connected to a microcontroller's XTAL pins.

Left Circuit (Sunny Electronics Corporation):

- The crystal (X101) is connected between X-TAL_2 and X-TAL_1.
- X-TAL_2 is connected to GND_2 via a 12pF capacitor (C113).
- X-TAL_1 is connected to GND_2 via a 12pF capacitor (C114).
- The microcontroller is CRYSTAL_BCM_Sunny EAW58812611.

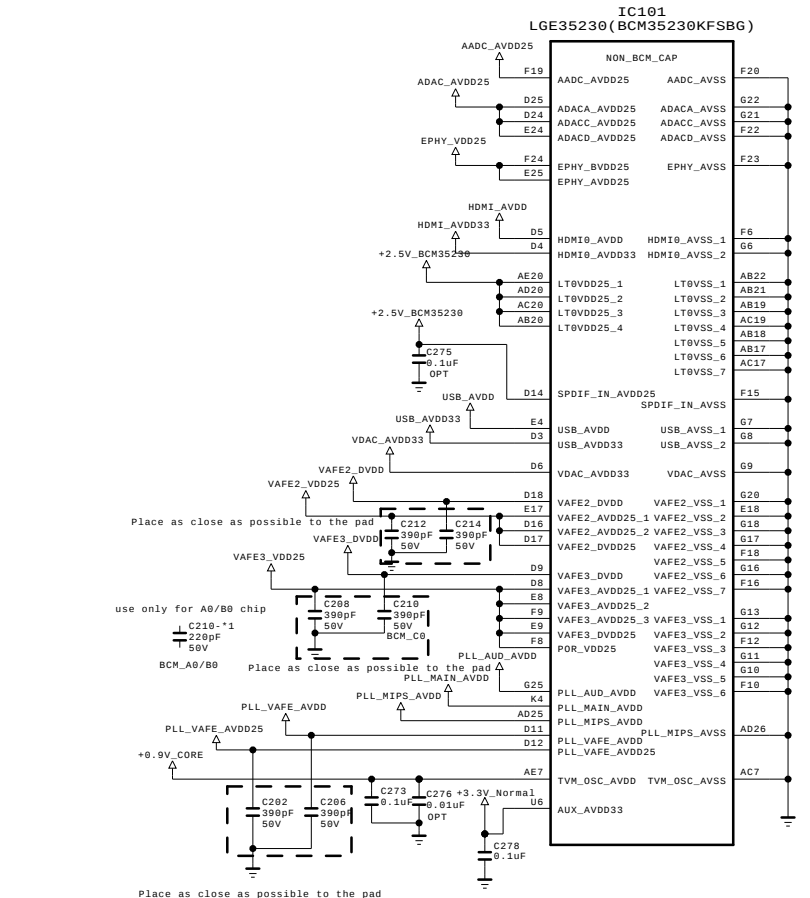
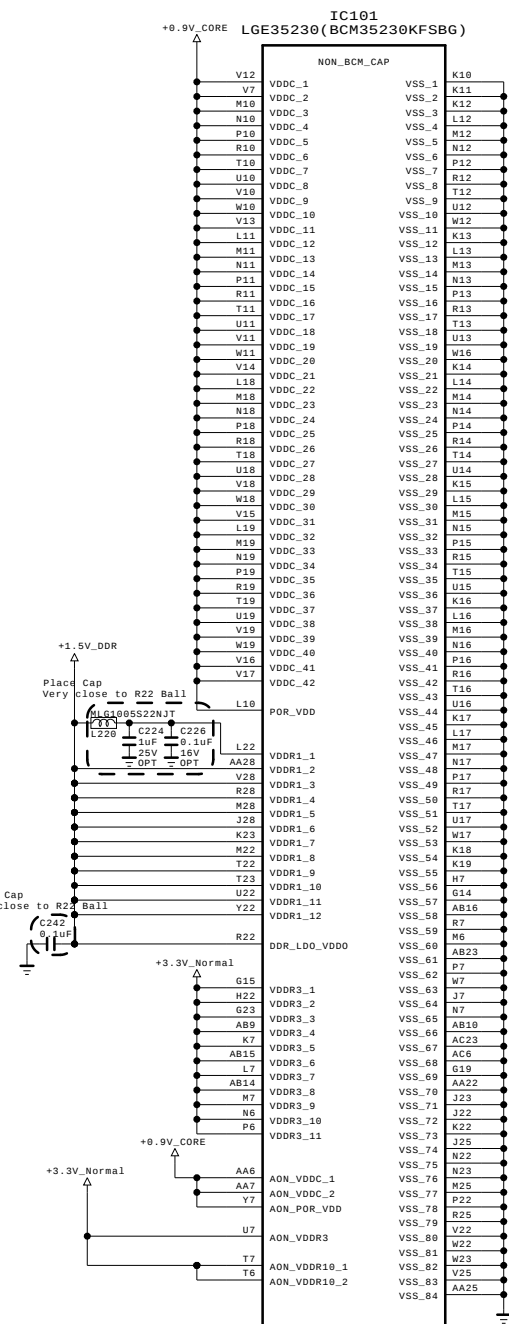
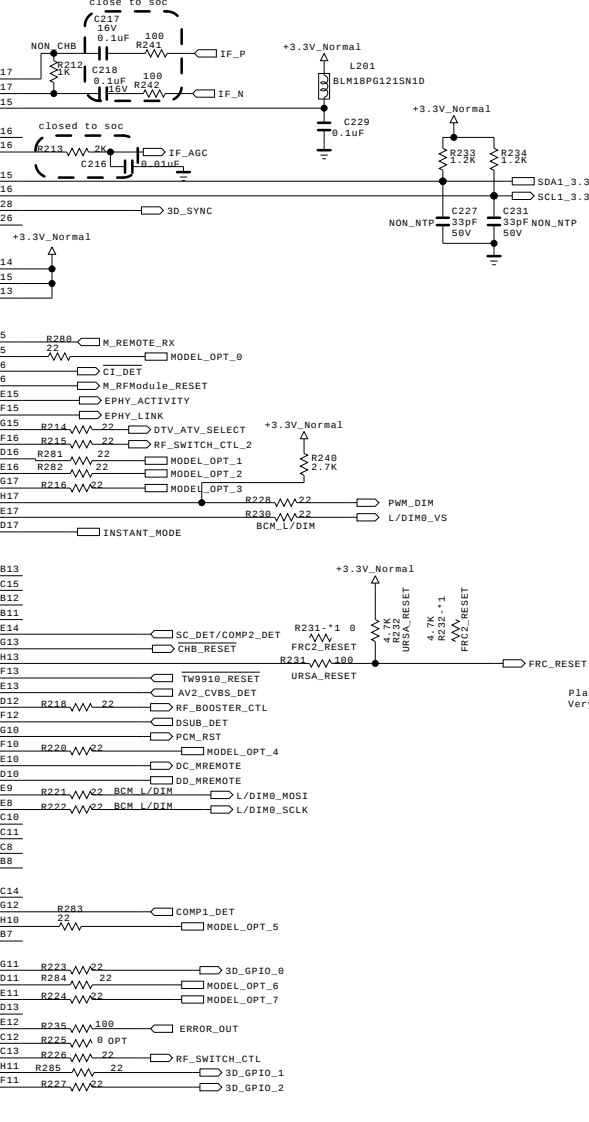
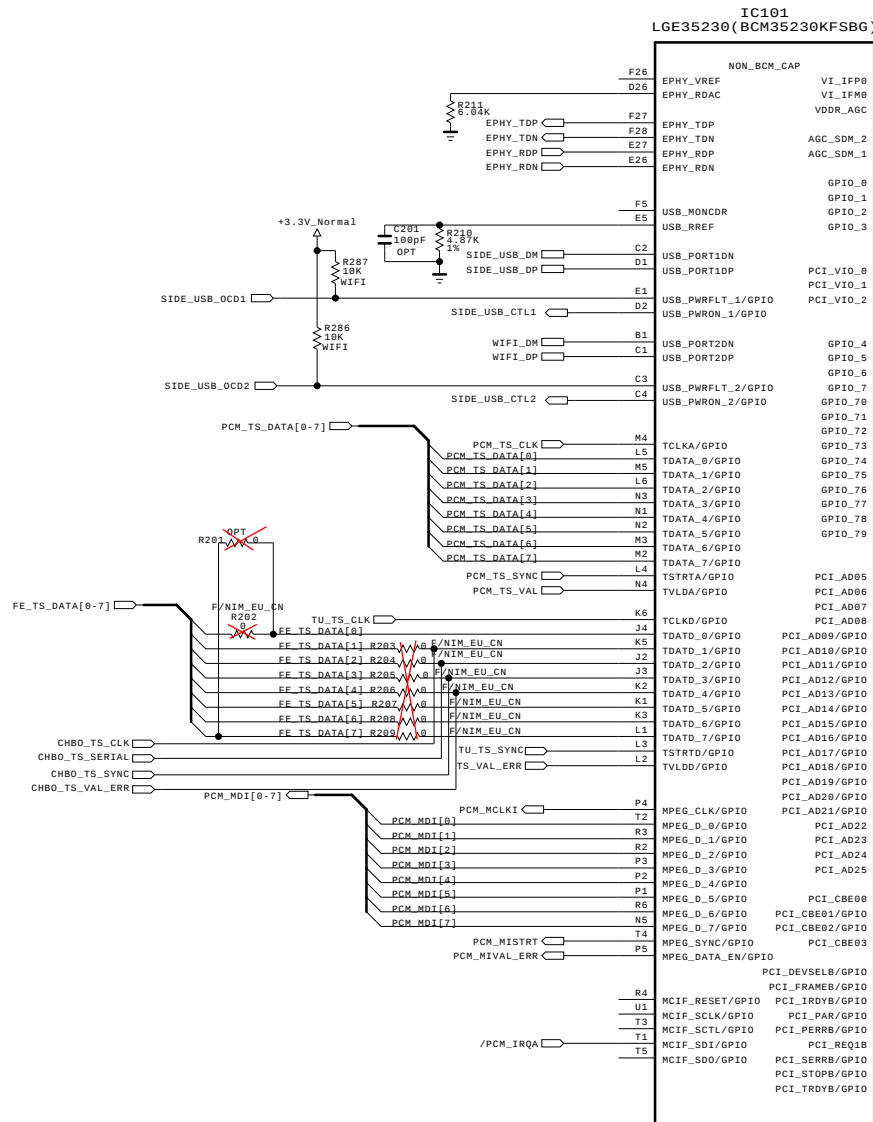
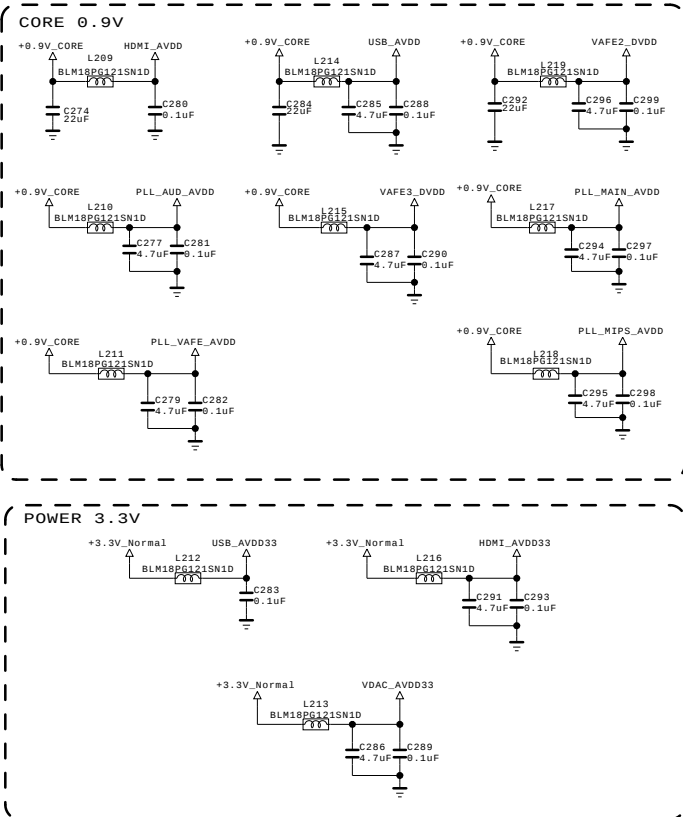
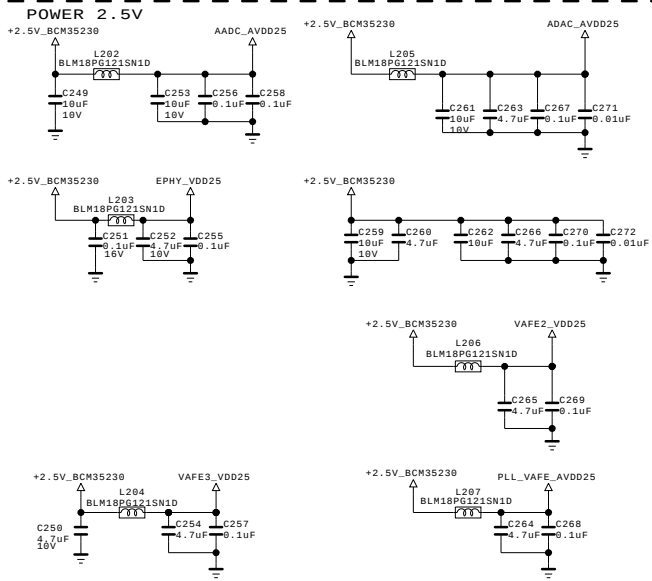
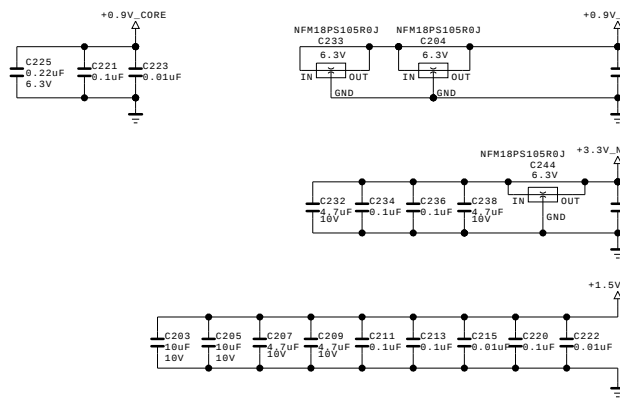
Right Circuit (DASHINMIKU CORPORATION):

- The crystal (X101) is connected between X-TAL_2 and X-TAL_1.
- X-TAL_2 is connected to GND_2 via a 12pF capacitor (C113).
- X-TAL_1 is connected to GND_2 via a 12pF capacitor (C114).
- The microcontroller is CRYSTAL_BCM_KDS EAW58239664.



MODEL OPTION			
	NO_FRC	BCM internal FRC	external URSAS
MODEL_OPT_0	0	0	1
MODEL_OPT_1	0	1	0
MODEL_OPT_2			
MODEL_OPT_3			
MODEL_OPT_4			
MODEL_OPT_5			
MODEL_OPT_6			
MODEL_OPT_7			

	HIGH	LOW
MODEL_OPT_2	FHD	HD
MODEL_OPT_3	OLED	LCD
MODEL_OPT_4	DDR speed	1333
MODEL_OPT_5	T2 Tuner	Support
MODEL_OPT_6	S Tuner	Support
MODEL_OPT_7	PHM	Enable



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MODEL	BCM35230	DATE	
BLOCK	MAIN POWER	SHEET	2 / 50

BCM35230_with_RGBCAP

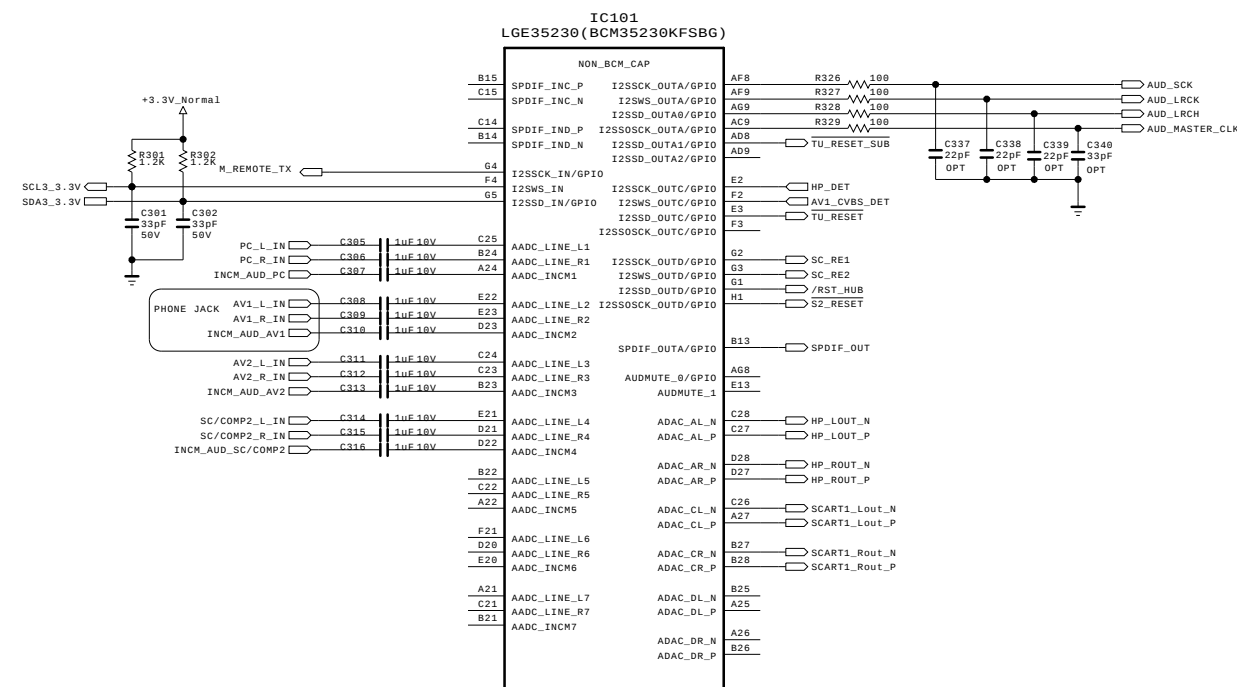
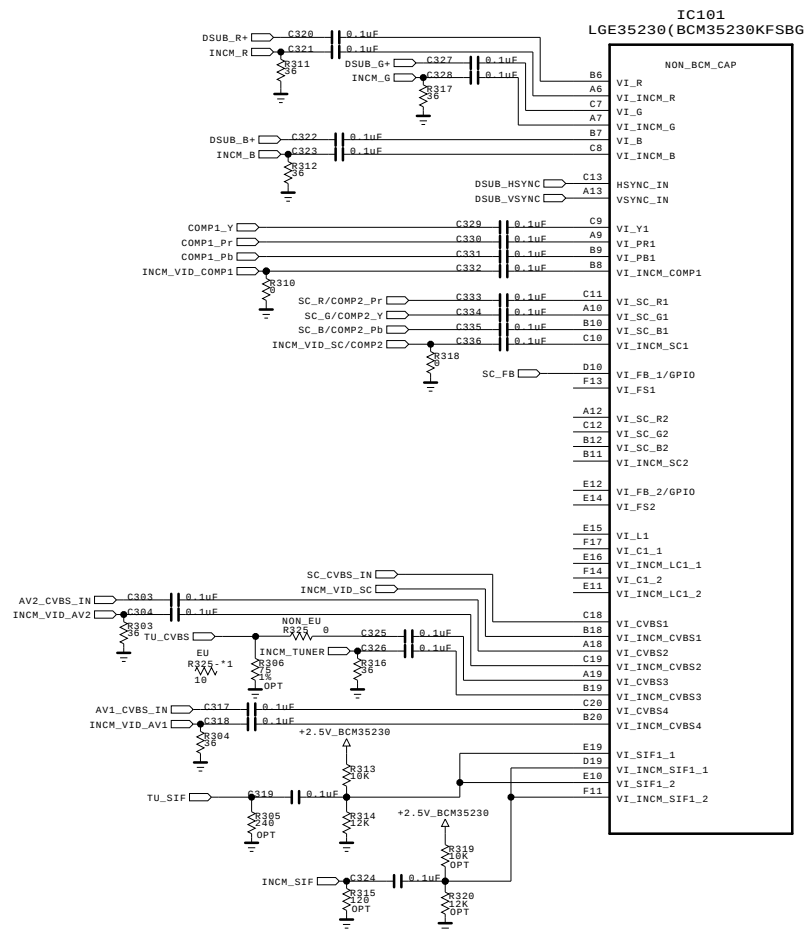
IC101- *1 LGE35230	
BCM_CAP	
B6	VI_R
A6	VI_INCM_R
C7	VI_G
A7	VI_INCM_G
B7	VI_B
C8	VI_INCM_B
C13	HSYNC_IN
A13	VSYNC_IN
C9	VI_Y1
A9	VI_PR1
B9	VI_PB1
B8	VI_INCM_COMP1
C11	VI_SC_R1
A10	VI_SC_G1
B10	VI_SC_B1
C10	VI_INCM_SC1
D18	VI_FB_1/GPIO
F13	VI_FS1
A12	VI_SC_R2
C12	VI_SC_G2
B12	VI_SC_B2
B11	VI_INCM_SC2
E12	VI_FB_2/GPIO
E14	VI_FS2
E15	VI_L1
F17	VI_C1_1
E16	VI_INCM_LC1_1
F14	VI_C1_2
E11	VI_INCM_LC1_2
C18	VI_CVBS1
B18	VI_INCM_CVBS1
A18	VI_CVBS2
C19	VI_INCM_CVBS2
A19	VI_CVBS3
B19	VI_INCM_CVBS3
C20	VI_CVBS4
B20	VI_INCM_CVBS4
E19	VI_SIF1_1
D19	VI_INCM_SIF1_1
E10	VI_SIF1_2
F11	VI_INCM_SIF1_2

VIDEO INCM

Near P801	Run Along DSUB_R Trace	INCM_R
Near P801	Run Along DSUB_G Trace	INCM_G
Near P801	Run Along DSUB_B Trace	INCM_B
Near JK1101	Run Along COMP_Y_IN,COMP_Pr_IN,COMP_Pb_IN Trace	INCM_VID_COMP1
Near JK1104	Run Along AV2_CVBS Trace	INCM_VID_AV2
Near TU2101/2 TU2201/2/3	Run Along TUNER_CVBS_IF_P Trace	INCM_TUNER
Near JK1102	Run Along AV1_CVBS Trace	INCM_VID_AV1
Near JK1103 JK2501	Run Along COMP_Y_IN,COMP_Pr_IN,COMP_Pb_IN/SC_R,G,B Trace	INCM_VID_SC/COMP2

AUDIO INCM

Near JK1102	Route Between AV1_L_IN & AV1_R_IN Trace	INCM_AUD_AV1
Near JK1103 JK2501	Route Between SC/COMP2_L_IN & SC/COMP2_R_IN Trace	INCM_AUD_SC/COMP2
Near JK1104	Route Between AV2_L_IN & AV2_R_IN Trace	INCM_AUD_AV2
Near JK801	Route Between PC_L_IN & PC_R_IN Trace	INCM_AUD_PC
Near TU2101/2 TU2201/2/3	Route Along With TUNER_SIF_IF_N	INCM_SIF



THE  SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE  SYMBOL MARK OF THE SCHEMATIC.

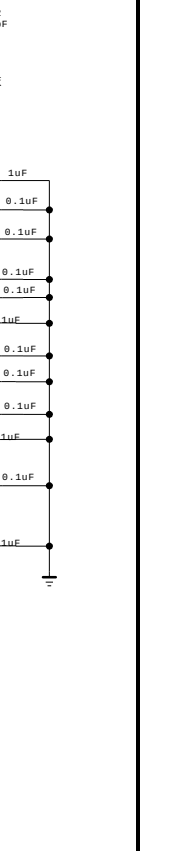
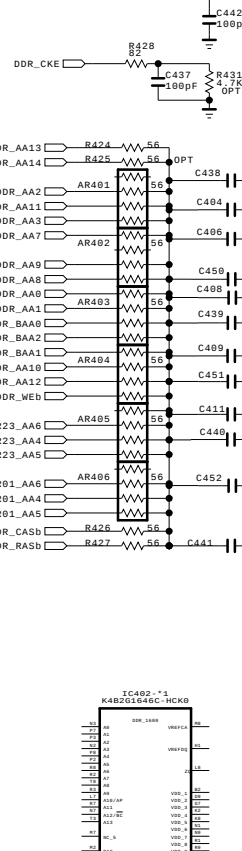
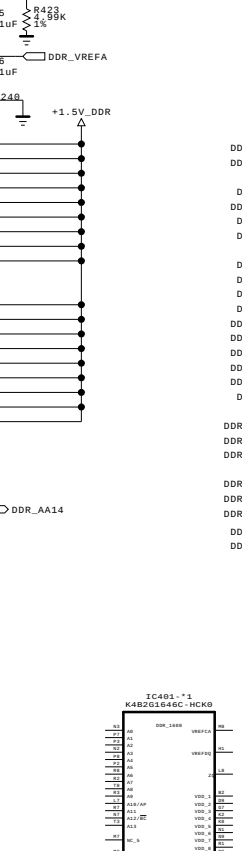
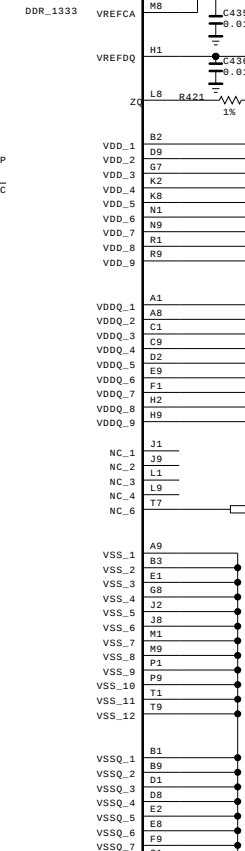
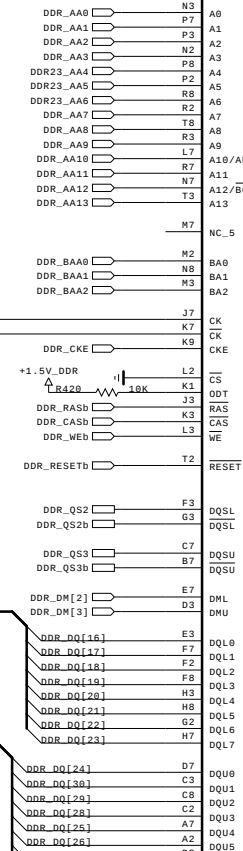
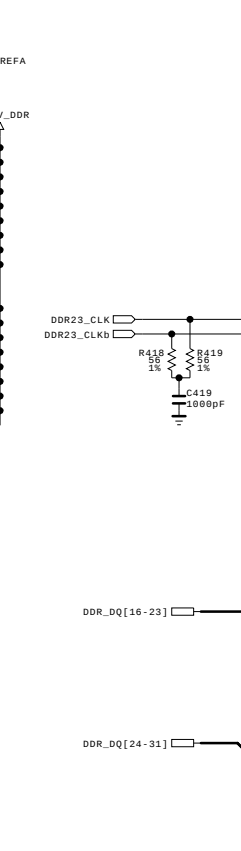
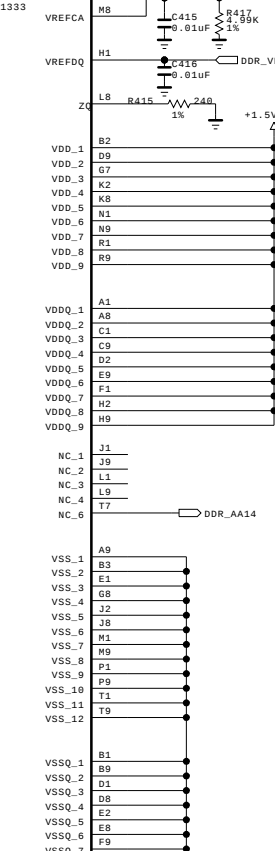
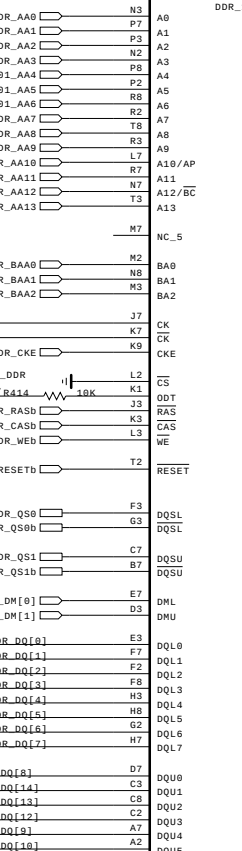
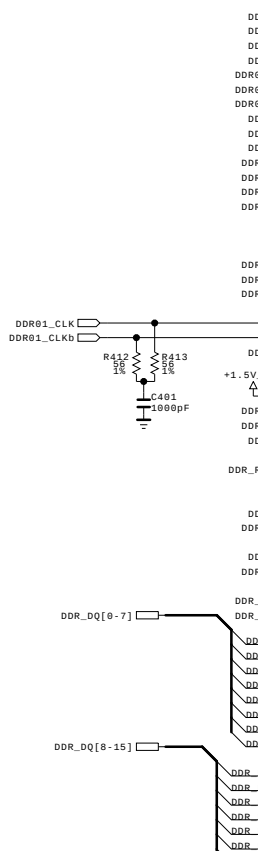
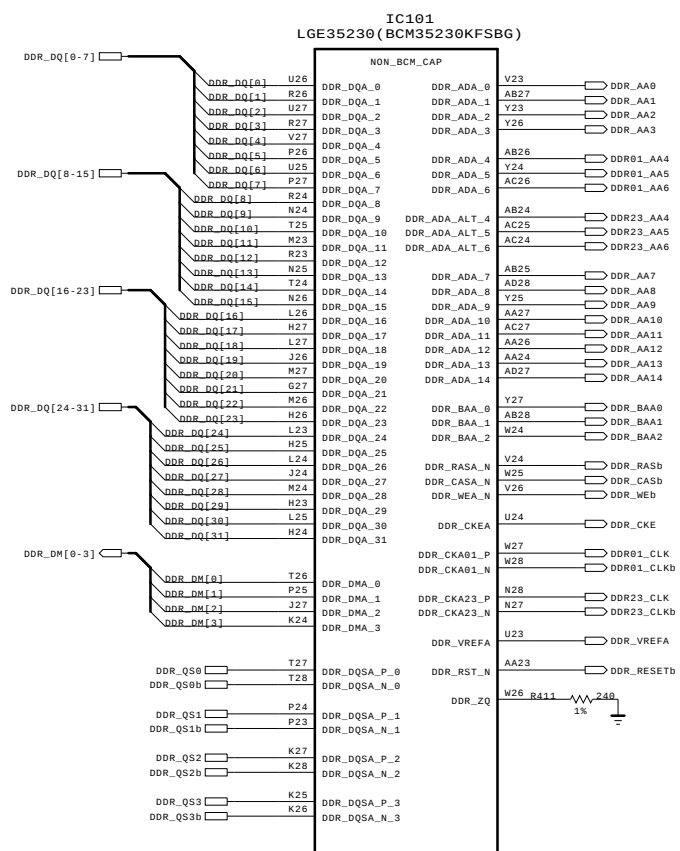
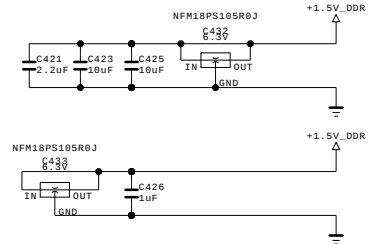
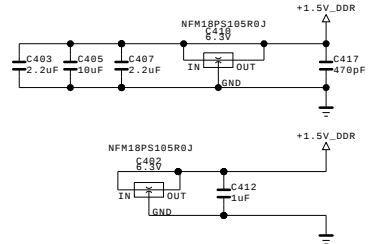
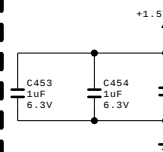
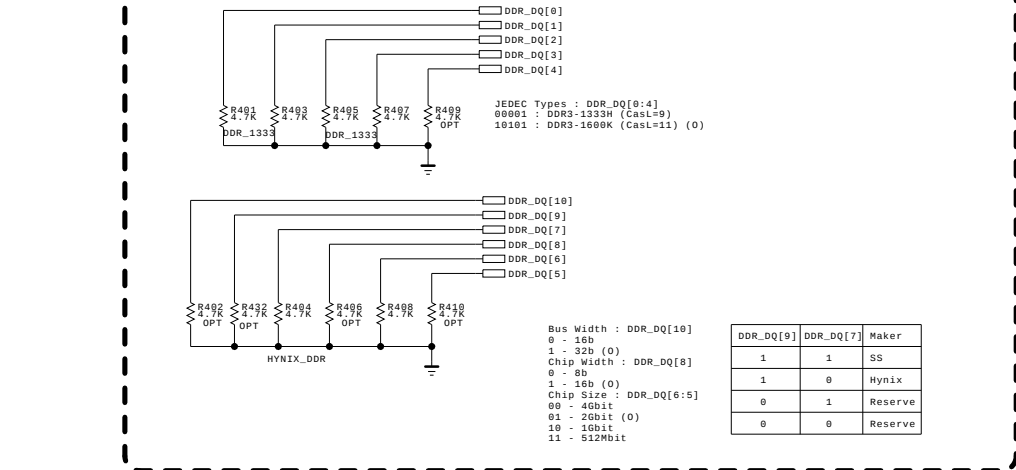
SECRET
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LG ELECTRONICS

MODEL	BCM35230	DATE	
BLOCK	MAIN AUDIO/VIDEO	SHEET	3 / 50

DUAL COMPONENT	
IC401, IC402	1ST : EAN61667501, 2ND : EAN61570701
IC401-*1 IC402-*1	1ST : T-K4B2G1646B_HCK0, 2ND : T-H5TQ2G63BFR-PBC

DDR STRAP



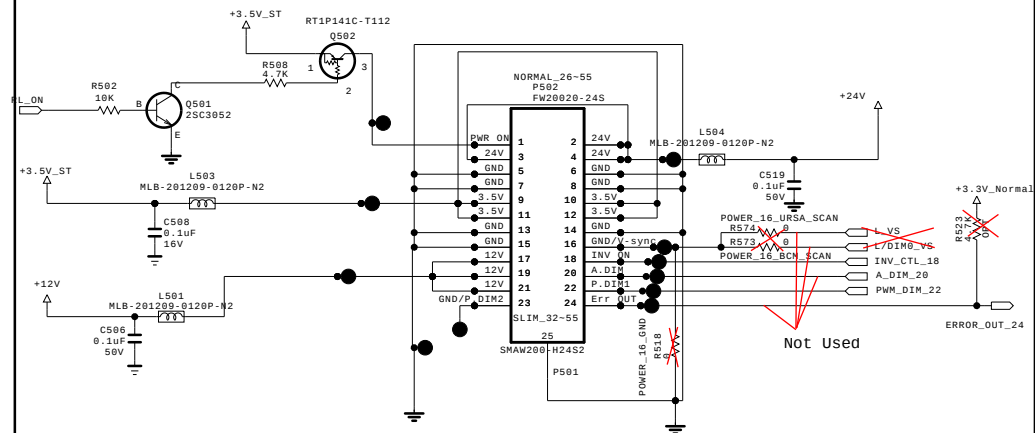
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SECRET
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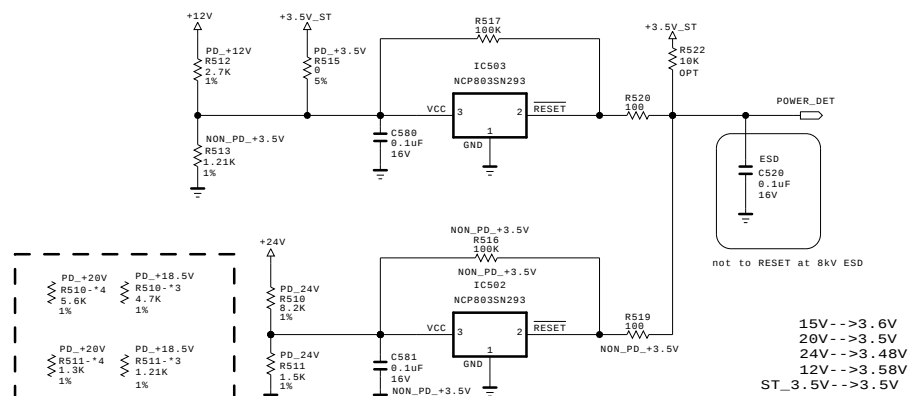
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BLOCK	MAIN DDR	SHEET	4 / 50

FROM LIPS & POWER B/D

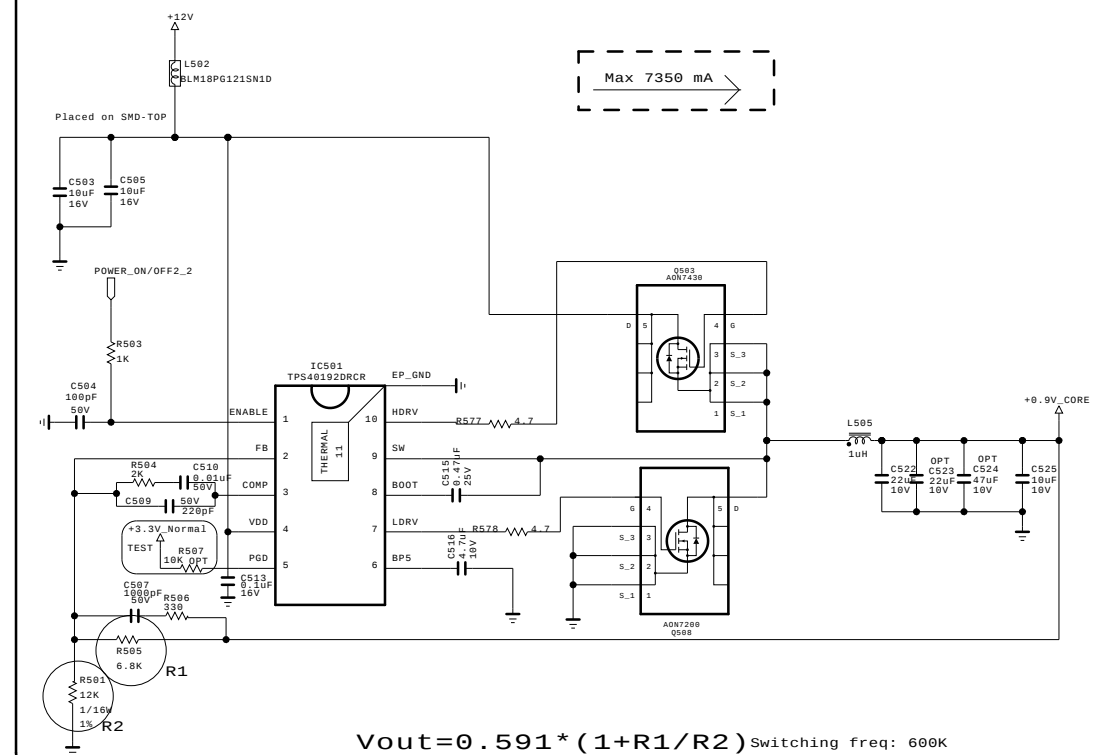


#16/#20/#23
LD - GND OR USE
LE(N.L.D.) - OPEN
LE(L.D.) - USE

Power_DET



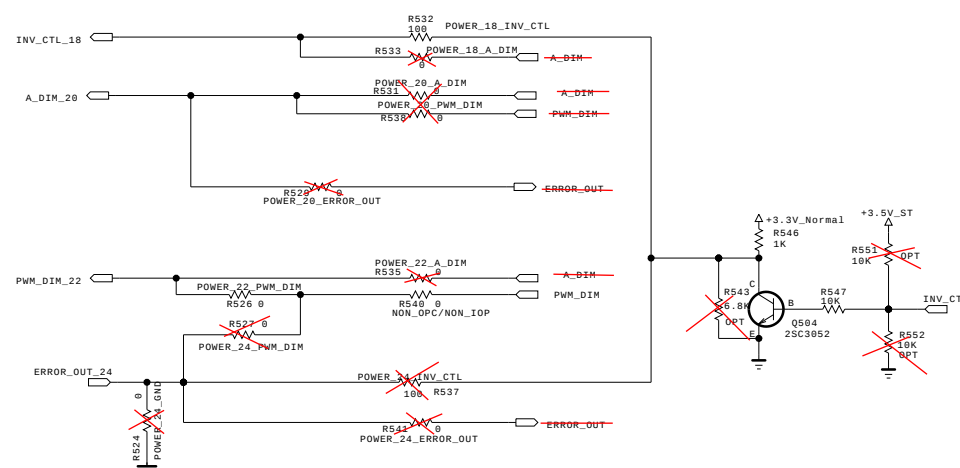
+0.9V_CORE_BCM35230



$V_{out} = 0.591 * (1 + R1/R2)$ Switching freq: 600K

DUAL COMPONENT		
Q501, Q504, Q505, Q506	1ST : 0TRIY80001A	2ND : 0TR387500AA
Q502	1ST : 0TRIY80004A, 2ND : EBK61012501, 3RD : 0TR102009AM	
Q507	1ST : EBK60752501, 2ND : EBK61011501	
IC502, IC503	1ST : EAN61151001, 2ND : EAN60670101	

OS Module OPT

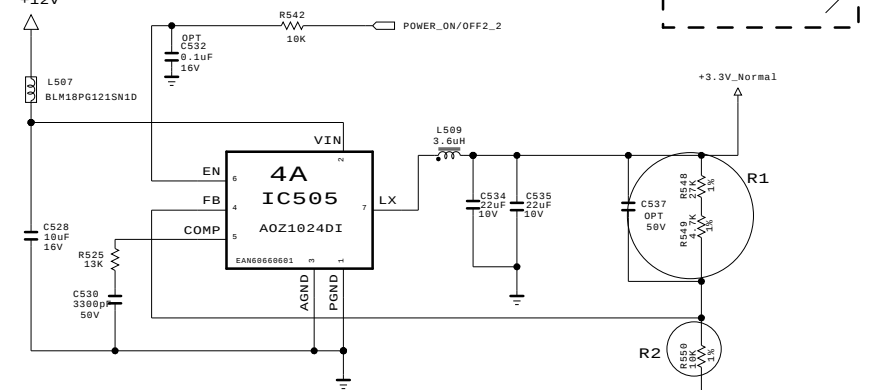


<OS MODULE PIN MAP>

PIN No	LG	CMO(09)	AUO	SHARP
18	INV_ON	A-DIM	INV_ON	INV_ON
20	V4:VBR-A V5:NC	NC	Err_out	Err_out
22	PWM_DIM	PWM_DIM	A-DIM	PWM_DIM
24	Err_out LED:GND	INV_ON	PWM_DIM	GND

CHECK PWR/MODULE PIN MAP

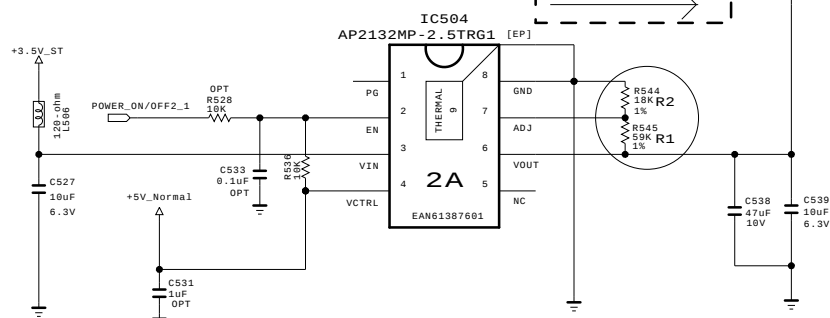
+3.3V_NORMAL



$V_{out} = 0.8 * (1 + R1/R2)$

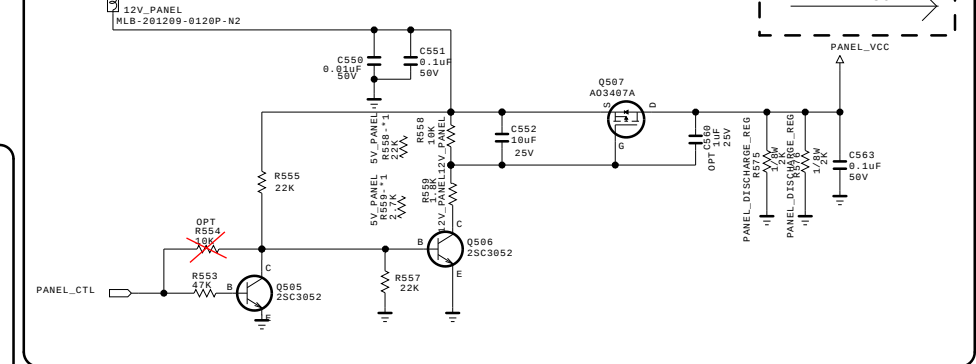
Switching freq: 500K

+2.5V_BCM35230

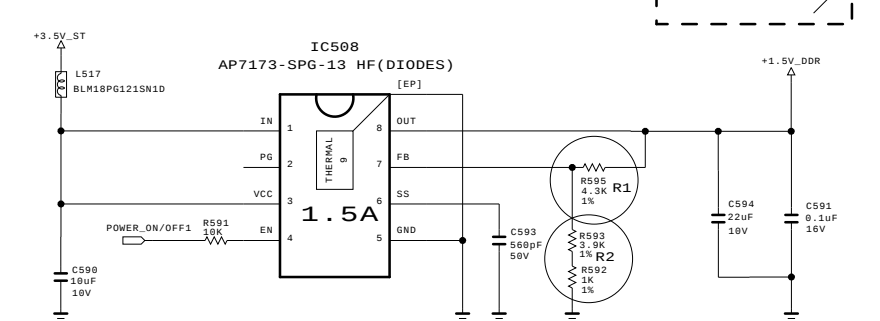


$V_{out} = 0.6 * (1 + R1/R2)$

PANEL_POWER

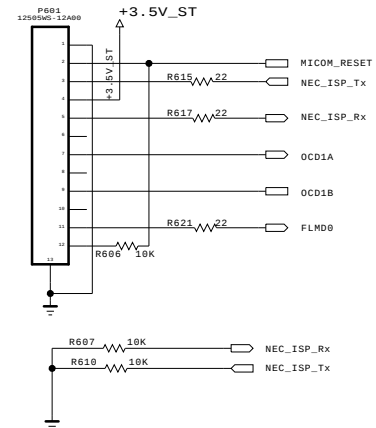


+1.5V_DDR

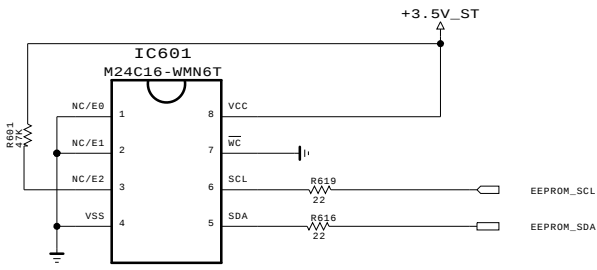


NEC MICOM

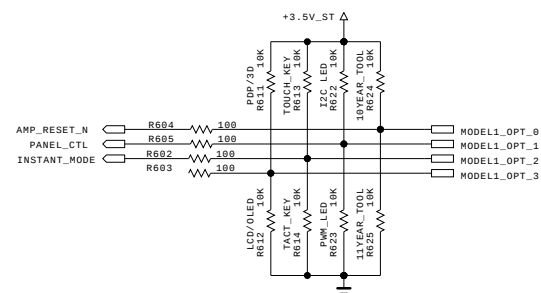
For Debug



EEPROM for Micom

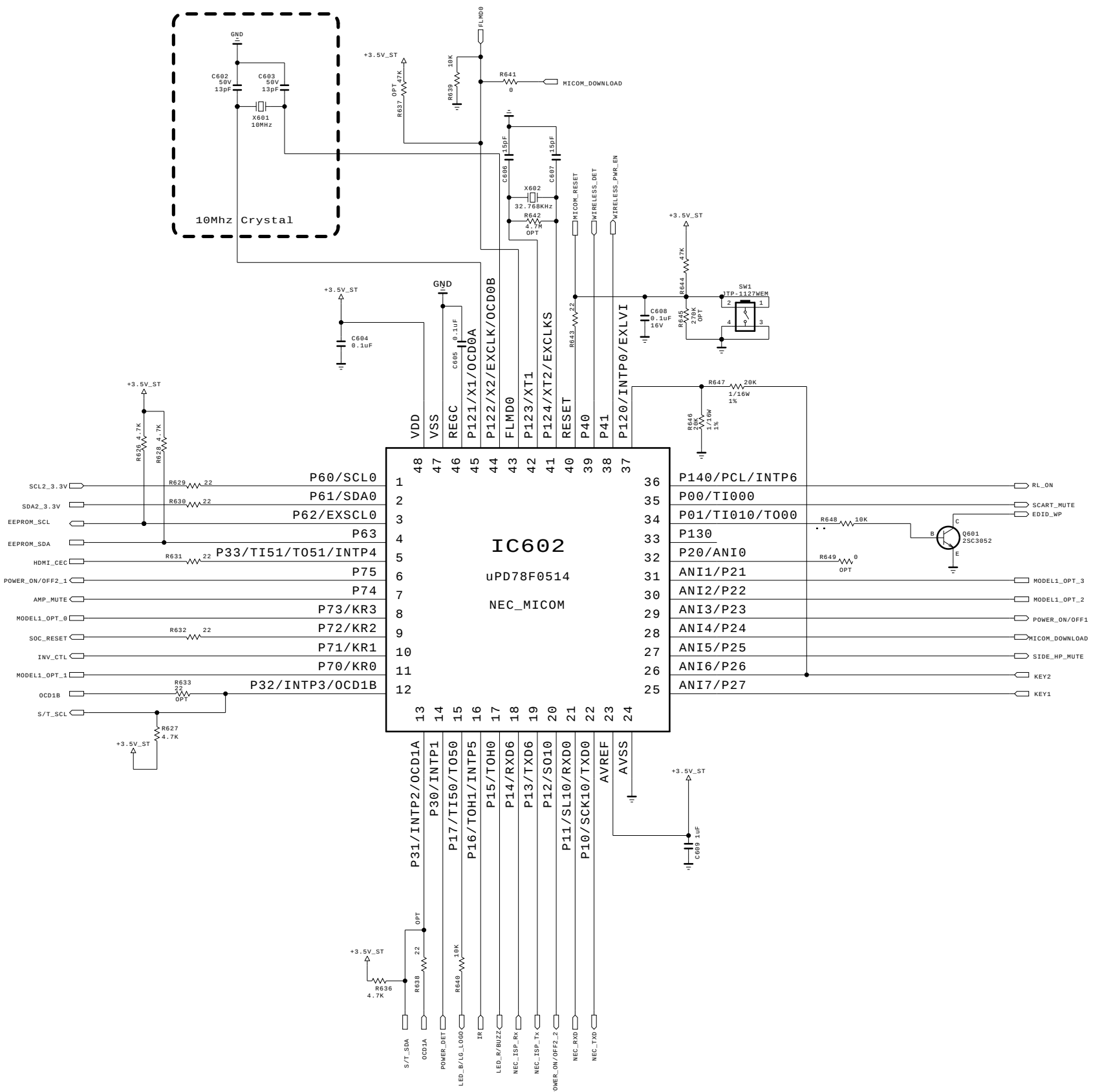


MICOM MODEL OPTION

MODEL OPTION

PIN NAME	PIN NO.	HIGH	LOW
MODEL_OPT_0	8	10YEAR_TOOL (10 SENSOR)	11YEAR_TOOL (11 SENSOR)
MODEL_OPT_1	11	I2C_LED	PWM_LED
MODEL_OPT_2	30	TOUCH_KEY	TACT_KEY
MODEL_OPT_3	31	PDP/3D	LCD/OLED

	LCD	PDP	OLED	3D
MODEL_OPT_3	0	1	0	1
	LOW	LOW_SMALL	TBD	HIGH
MODEL_OPT_1	0	0	1	1
MODEL_OPT_2	0	1	0	1

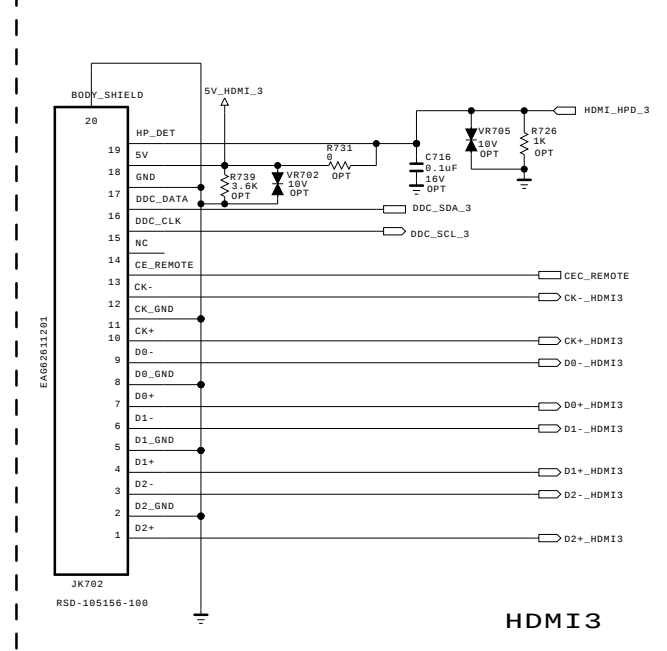
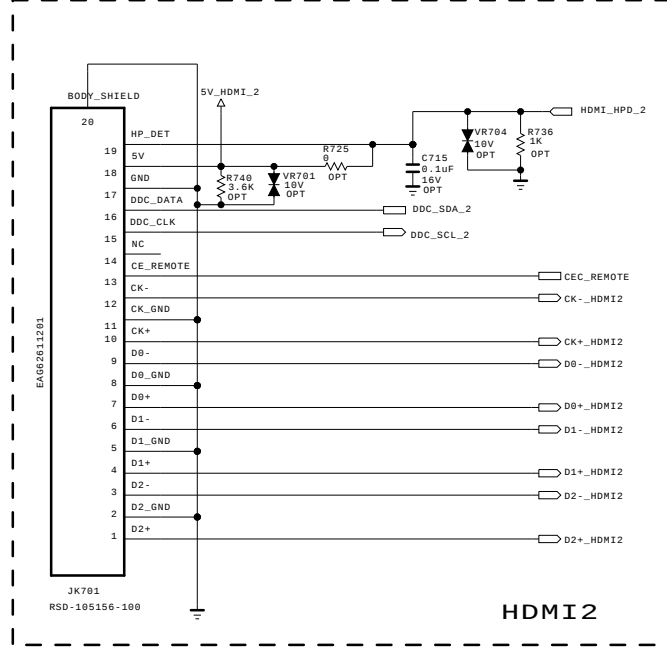
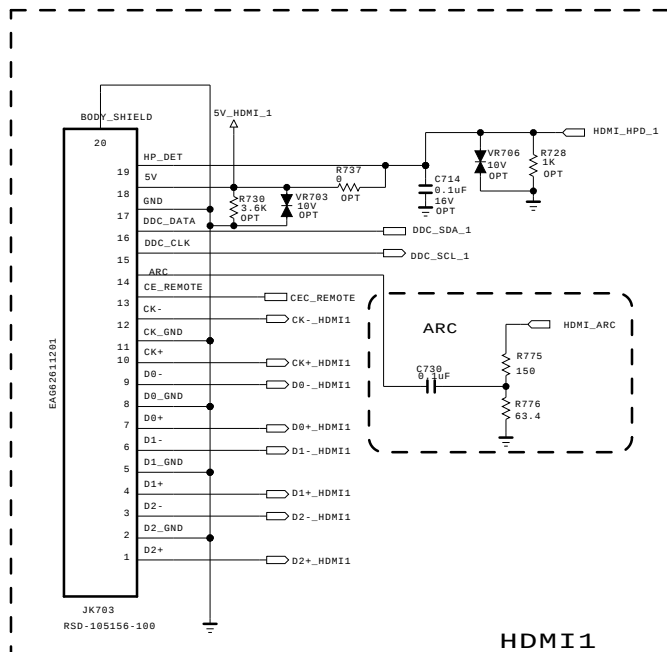


THE ⚠ SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE ⚠ SYMBOL MARK OF THE SCHEMATIC.

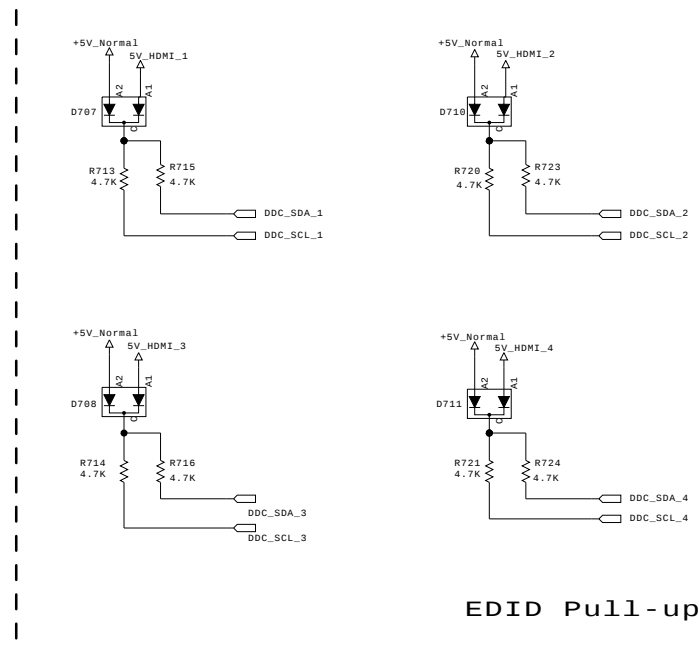
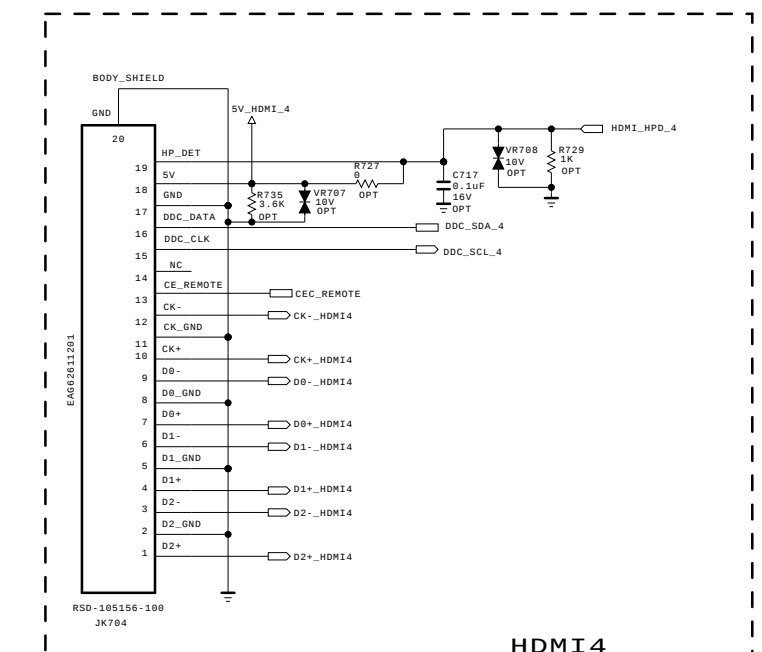
SECRET
LGElectronics



MODEL	BCM35230	DATE	
BLOCK	MICOM	SHEET	6 / 50



THE SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE SYMBOL MARK OF THE SCHEMATIC.

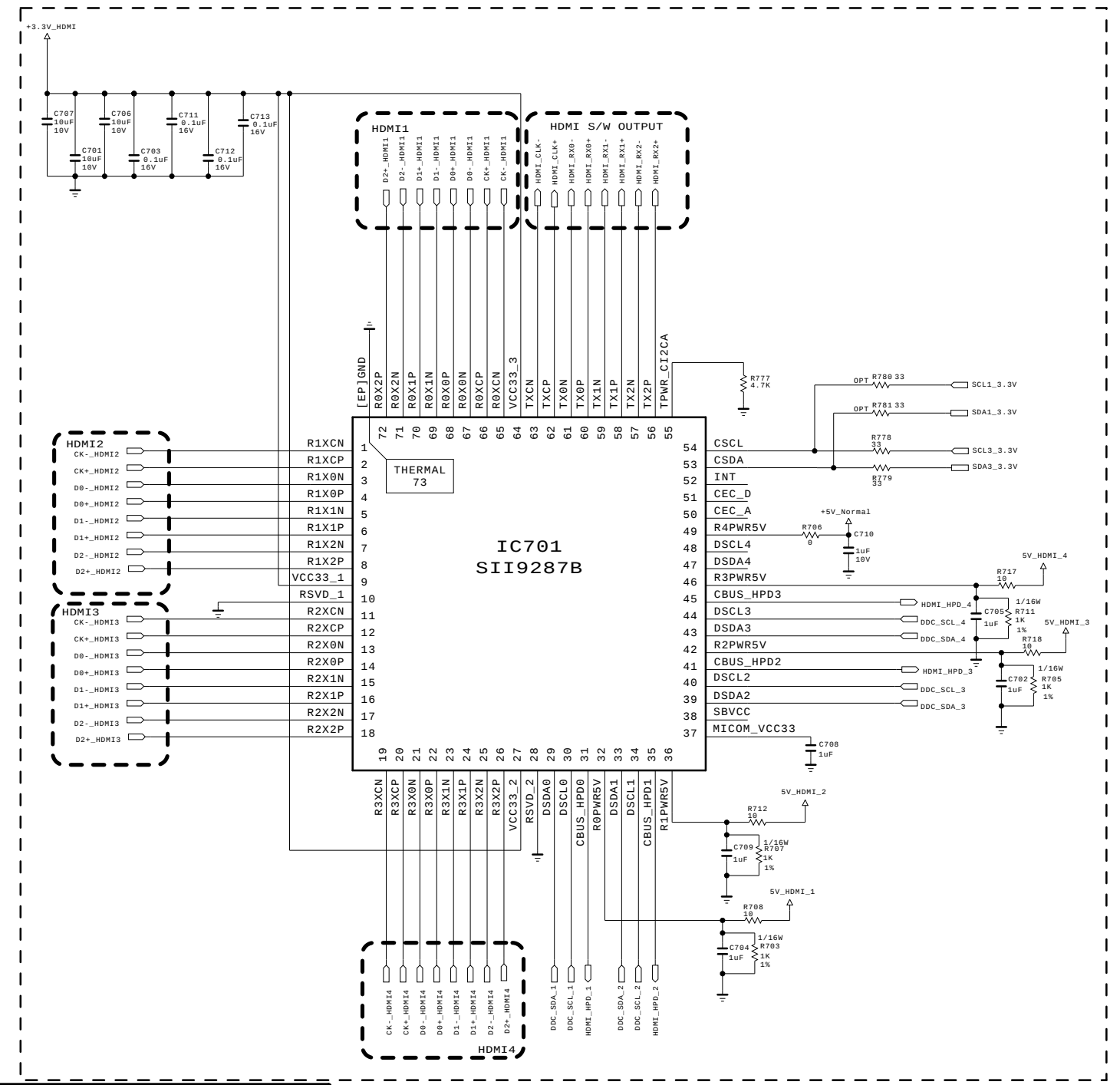
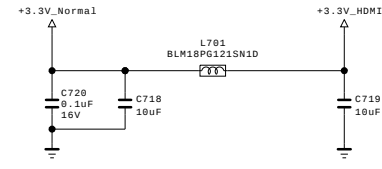
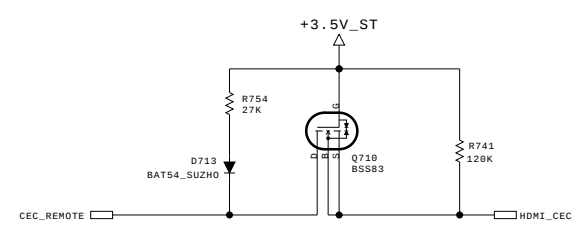


SECRET
LGElectronics



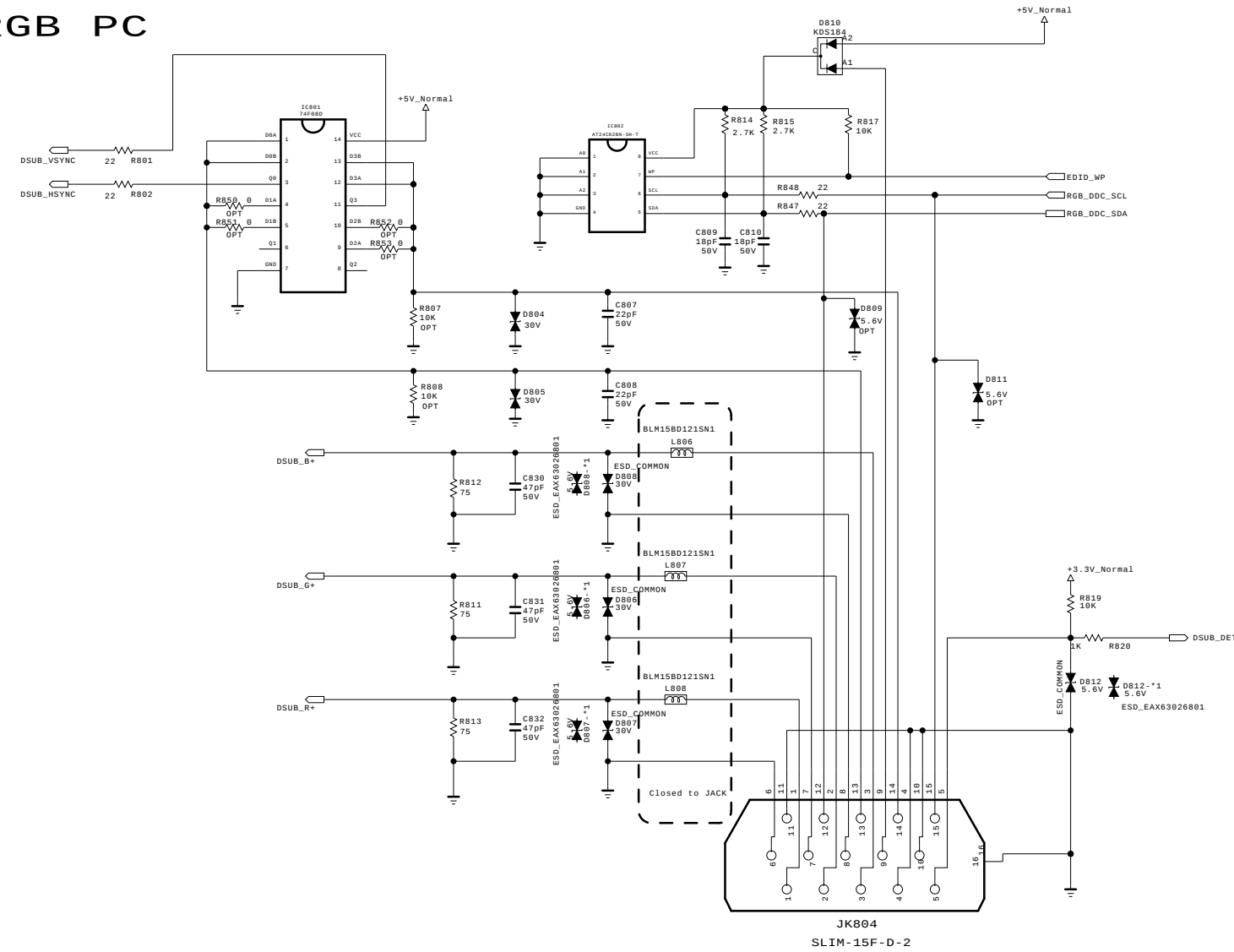
DUAL COMPONENT	
D707, D708 D710, D711	1ST : 0DD184009AA 2ND : 0DSIH00028A
D713	1ST : T-BAT54_SUZH0, 2ND : 0DS0N00138A

* HDMI CEC

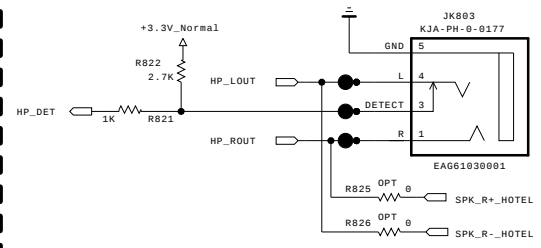


MODEL	BCM35230	DATE	
BLOCK	HDMI	SHEET	7 / 31

RGB PC

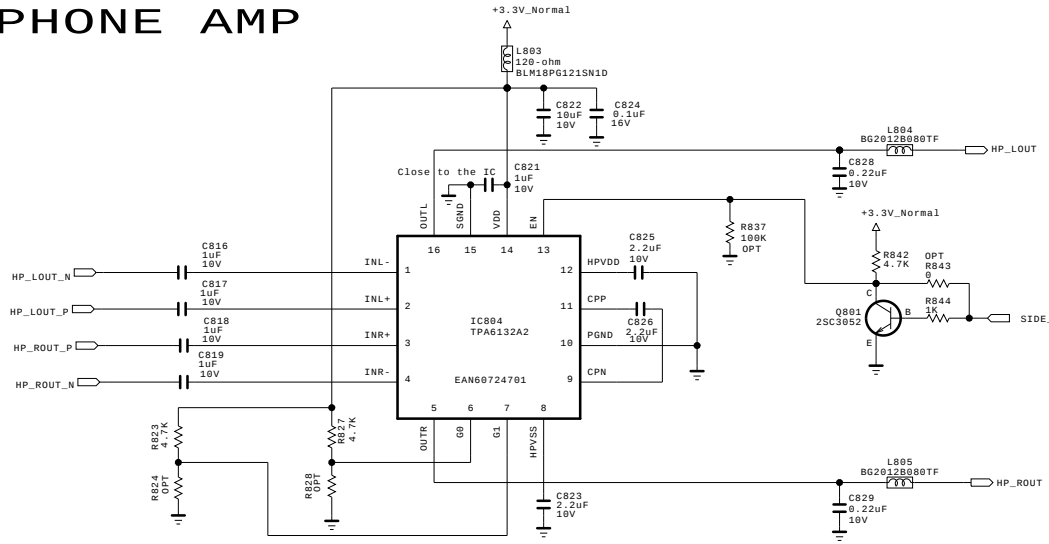


EARPHON JACK

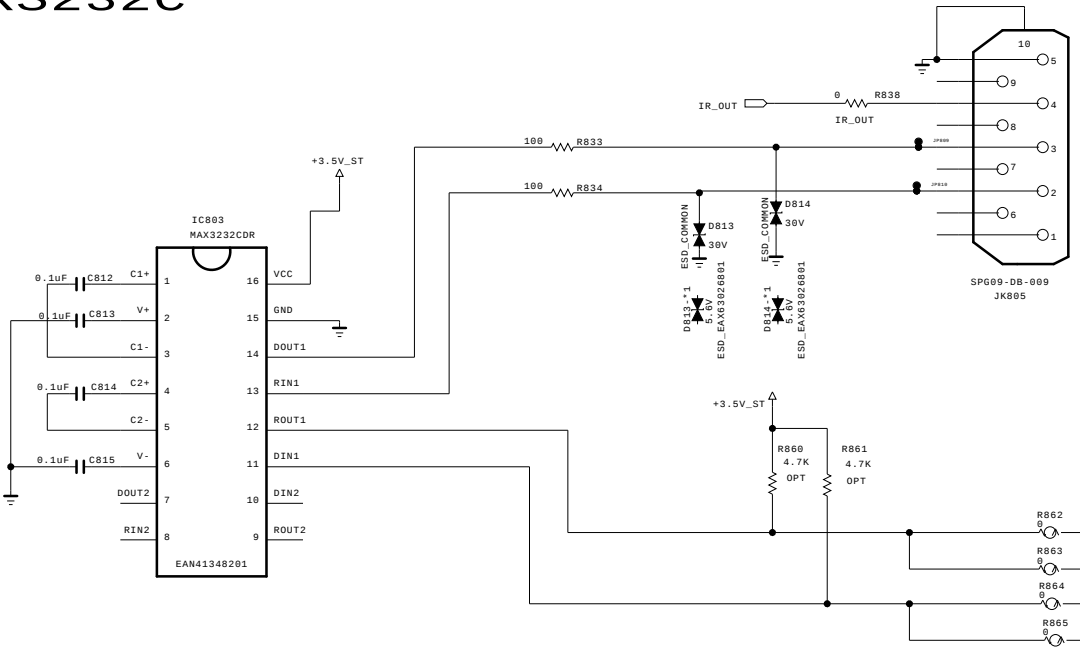


DUAL COMPONENT	
D804, D805, D806 D807, D808, D813 D814	1ST : EAH39491601, 2ND : EAH33945901
D810	1ST : 0DD184009AA, 2ND : 0DSIH00028A
Q801	1ST : 0TRIY80001A, 2ND : 0TR387500AA
IC805	1ST : EAN61151201, 2ND : EAN61130001

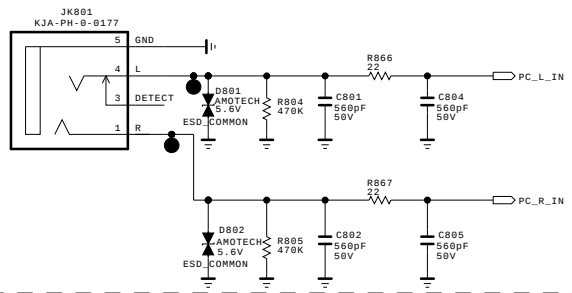
EARPHONE AMP



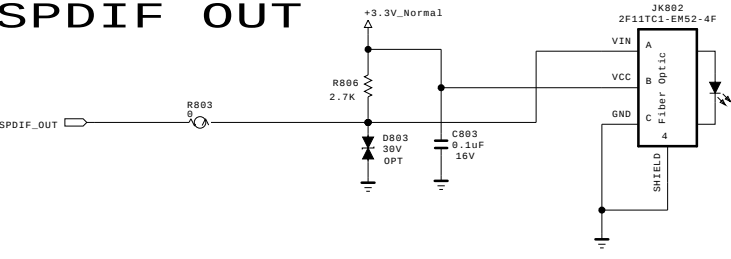
RS232C



PC AUDIO



SPDIF OUT



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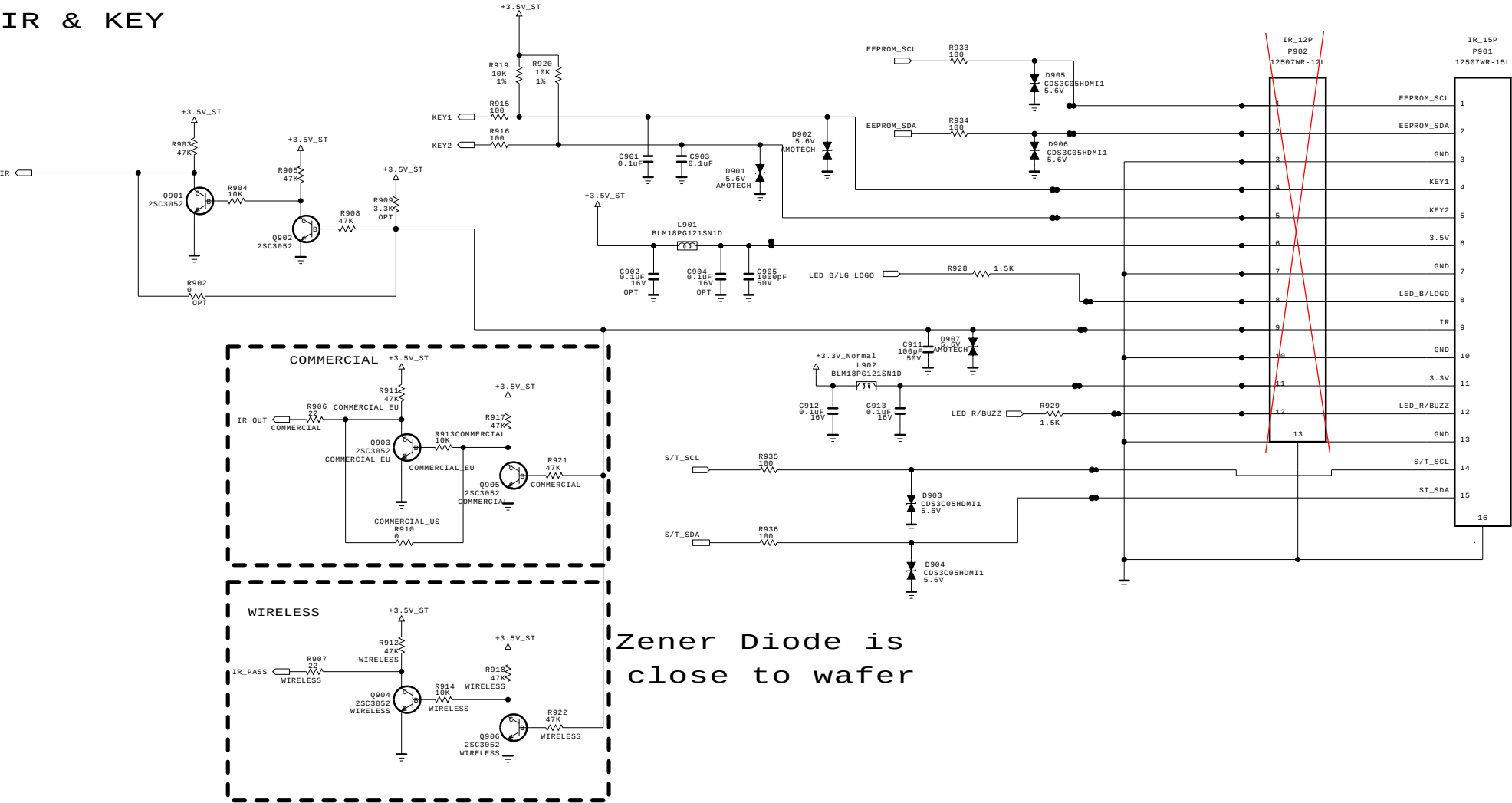
SECRET
LGElectronics

 LG ELECTRONICS

MODEL	BCM35230	DATE	2010.10.21
BLOCK	COMMON JACK	SHEET	8 / 58

DUAL COMPONENT	
Q901,Q902,Q903 Q904,Q905,Q906	1ST : 0TRIY80001A 2ND : 0TR387500AA
D903,D904 D905,D906	1ST : EAH42720601, 2ND : EAH60994401

IR & KEY



Zener Diode is
close to wafer

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SECRET
LGElectronics

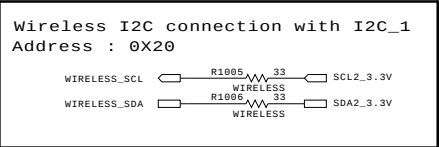
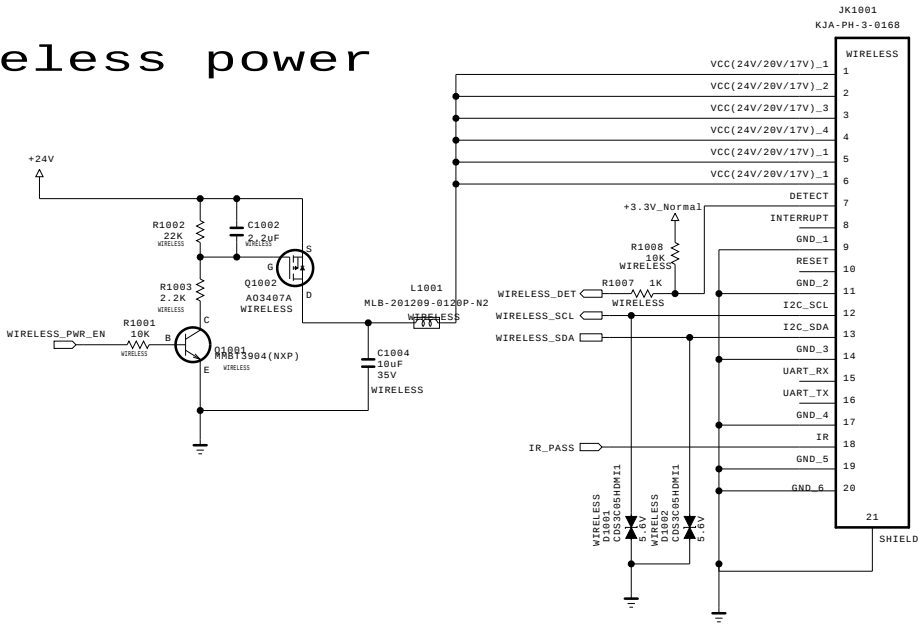


MODEL	BCM35230	DATE	
BLOCK	IR/KEY	SHEET	9 / 50

WIRELESS READY MODEL

DUAL COMPONENT		
D1001,D1002	1ST : EAH42720601	2ND : EAH60994401
Q1001	1ST : EBK61012601,	2ND : 0TRDI80002A
Q1002	1ST : EBK60752501,	2ND : EBK61011501

wireless power



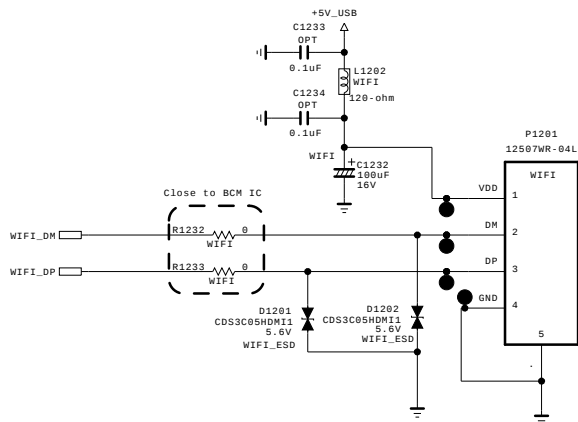
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The schematic diagram illustrates the SC/COMP2 board layout. It features a central horizontal bus connected to various components. On the left, there are three 5.1V diodes (ZD1103, ZD1104, ZD1105, ZD1106, ZD1107, ZD1108) and a 5.6V diode (D1112, D1113, D1114). The board is divided into sections for SC/COMP2_R_IN, SC/COMP2_L_IN, SC_R/COMP2_Pr, SC_B/COMP2_Pb, SC_G/COMP2_Y, and SC_DET/COMP2_DET. A dashed box labeled "FOR EMI" encloses the SC_R/COMP2_Pr, SC_B/COMP2_Pb, and SC_G/COMP2_Y sections. The diagram includes a 3.3V Normal supply and a 5.6V diode.

SECRET
LGElectronics



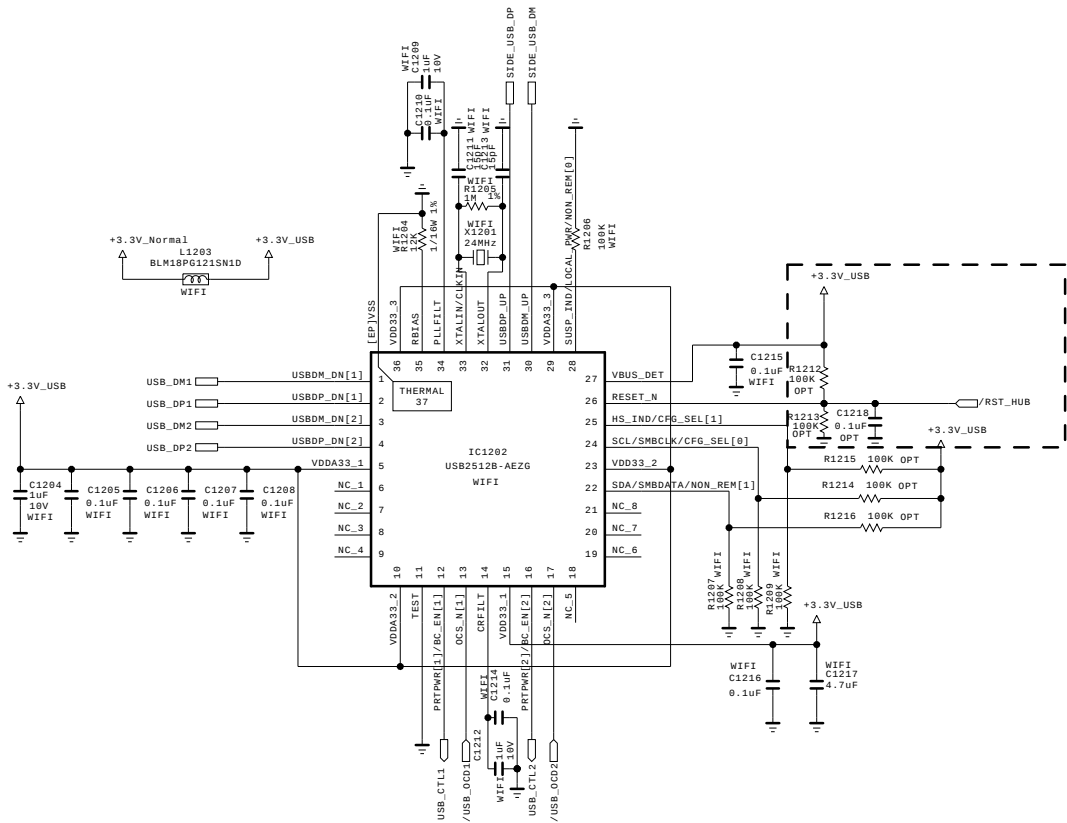
USB_WIFI



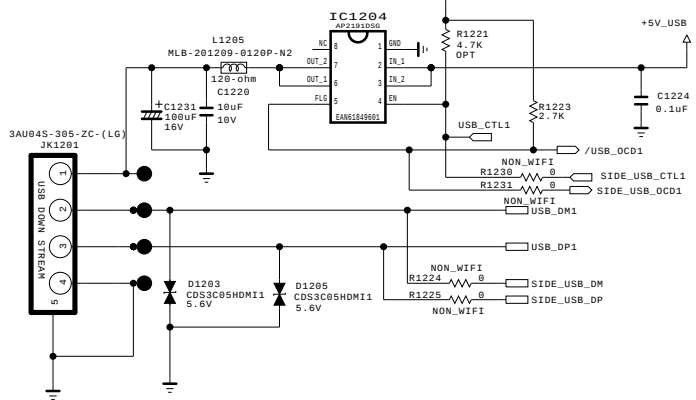
DUAL COMPONENT

D1201, D1202
D1203, D1204
D1205, D1206

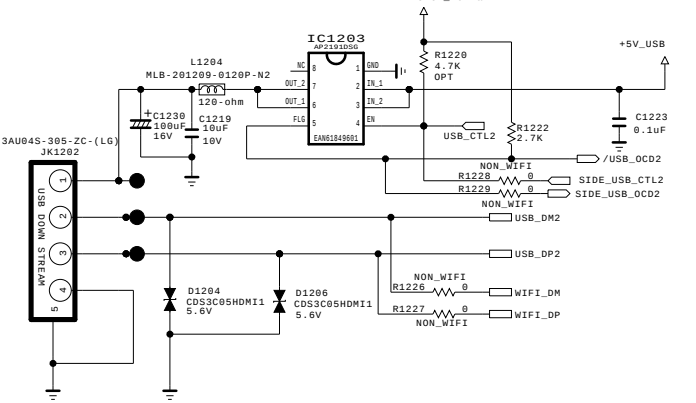
1ST : EAH42720601 2ND : EAH60994401



USB / DVR Ready



USB



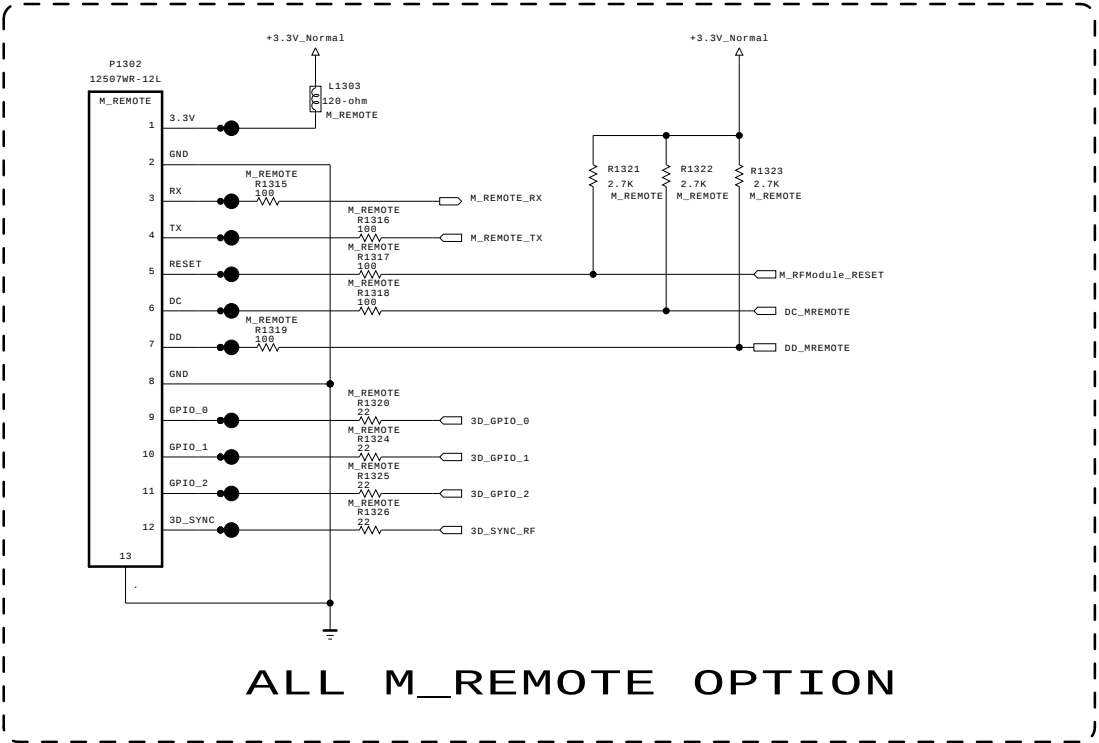
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SECRET
LGElectronics



MODEL	BCM35230	DATE	
BLOCK	USB + WIFI	SHEET	12 / 50

TI solution M_REMOTE OPTION



ALL M_REMOTE OPTION

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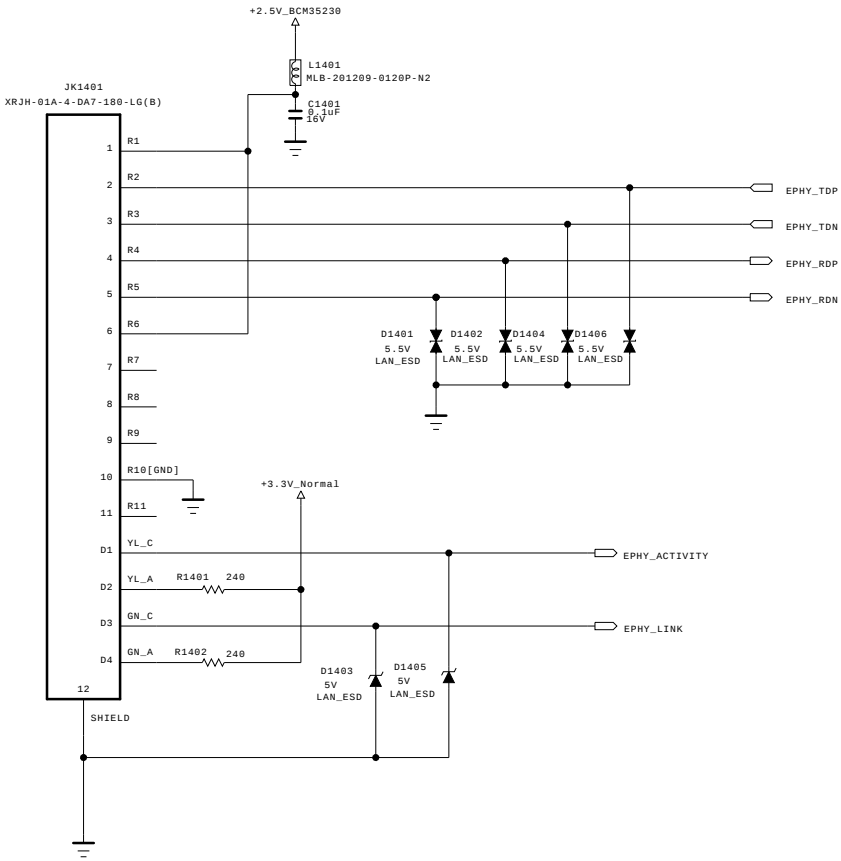
SECRET
LGElectronics



MODEL	BCM35230	DATE	
BLOCK	M_REMOCON	SHEET	13 / 50

Ethernet Block

DUAL COMPONENT		
D1401, D1402 D1403, D1404 D1405, D1406	1ST : EAH42720601	2ND : EAH60994401



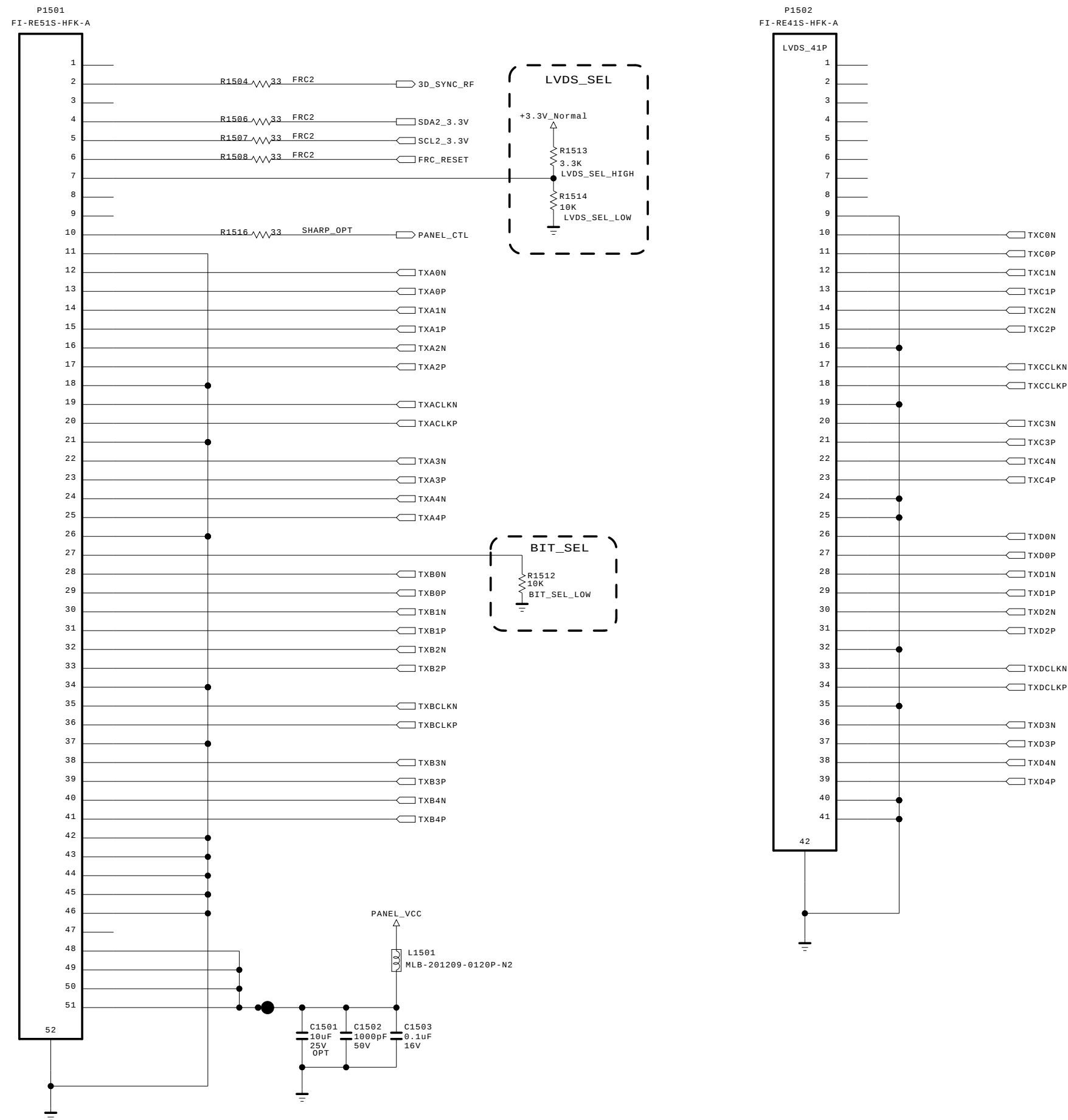
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SECRET
LGElectronics



MODEL	BCM35230	DATE	
BLOCK	ETHERNET	SHEET	14 / 50

FHD120Hz LVDS output(51pin+41Pin)



REVERSE MARK
LVDS_41P
C1504
2.2uF
50V

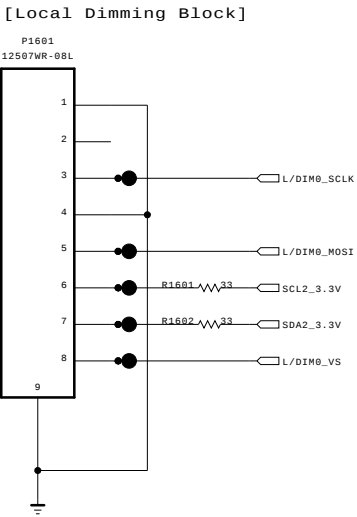
THE ⚠ SYMBOL MARK OF THIS SCHEMETIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE ⚠ SYMBOL MARK OF THE SCHEMETIC.

SECRET

LG Electronics

LG ELECTRONICS

MODEL	BCM35230	DATE	2010.11.03
BLOCK	LVDS	SHEET	15 / 50



THE  SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE  SYMBOL MARK OF THE SCHEMATIC.

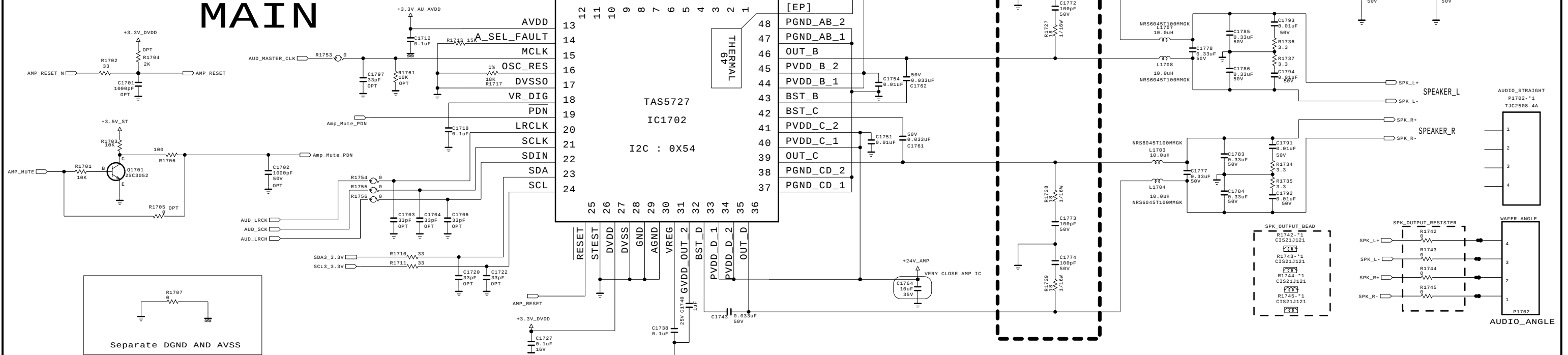
SECRET

LGElectronics



MODEL	BCM35230	DATE	
BLOCK	L_DIMMING	SHEET	16 / 50

DUAL COMPONENT	
Q1701	1ST : 0TRIY80001A 2ND : 0TR387500AA



WOOFER

Separate DGND AND AVSS

This parts are Located on AVSS area.

WOOFER

TAS5727
IC1701

I2C : 0X56

WOOFER

Place at Bottom

WOOFER_OUTPUT_BEAD

WOOFER_OUTPUT_RESISTOR

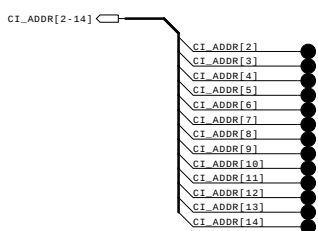
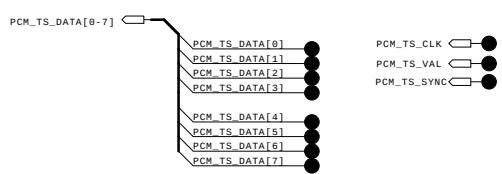
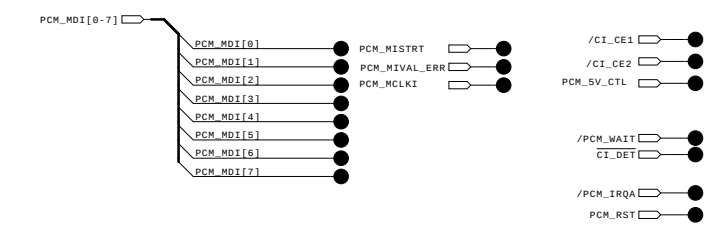
WOOFER

P1701
SHAN250-05

SECRET
LGElectronics



NON CI



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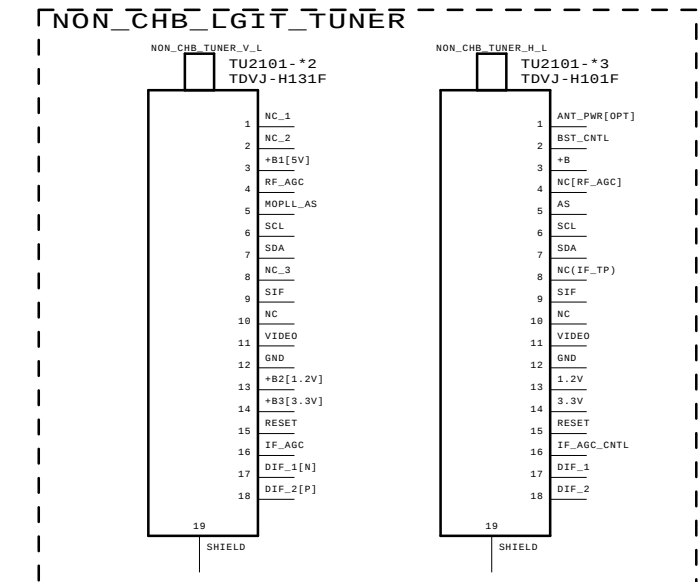
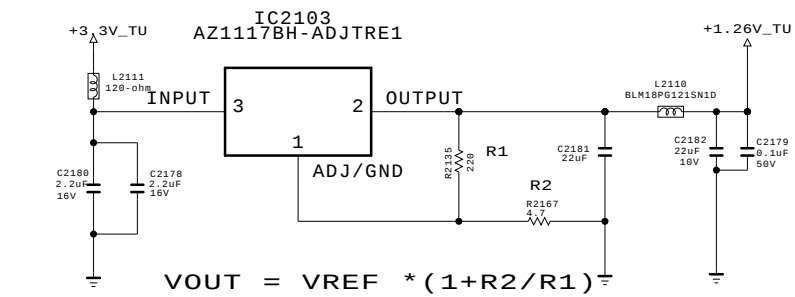
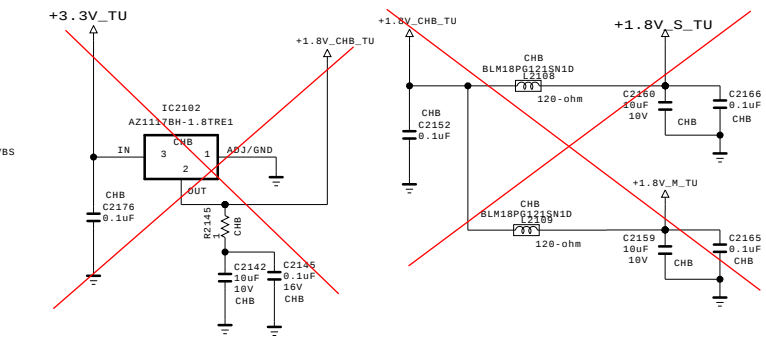
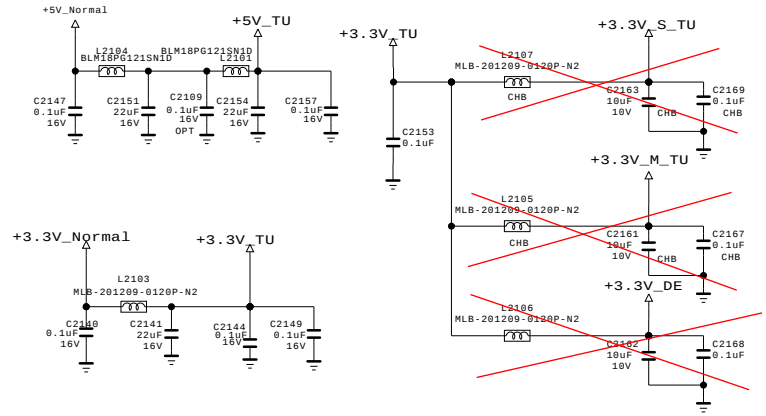
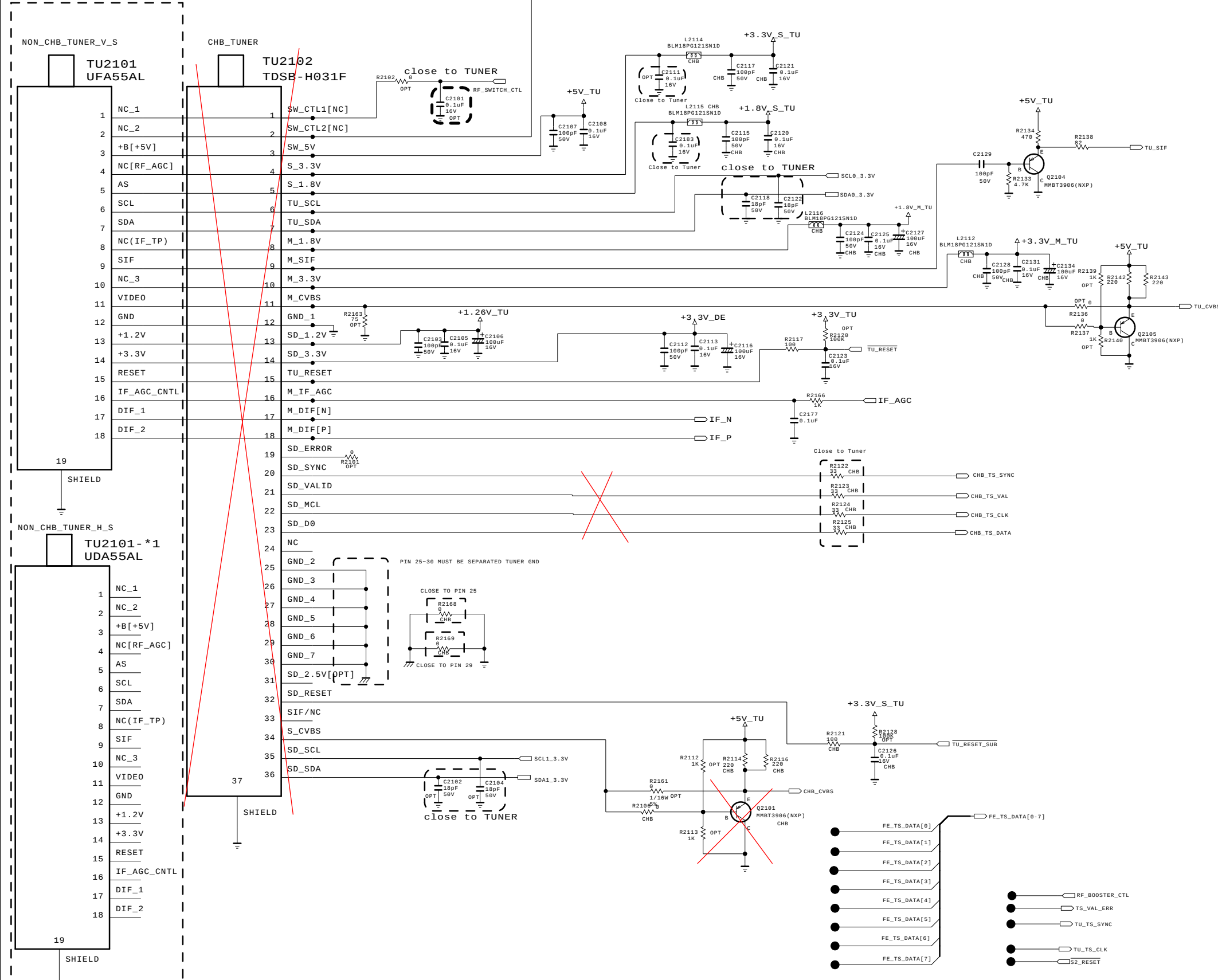
SECRET
LGElectronics



MODEL	BCM35230	DATE	
BLOCK	NON CI	SHEET	20 / 50

NON_CHB & CHB TUNER (KOR/USA)

DUAL COMPONENT	
IC2102	1ST : EAN61829801, 2ND : EAN544289201
IC2103	1ST : EAN61507501, 2ND : EAN54428101



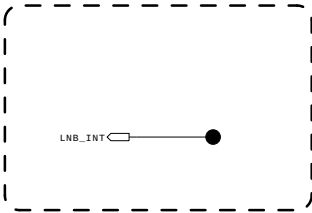
THE  SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FIRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE  SYMBOL MARK OF THE SCHEMATIC.

SECRET
LGElectronics

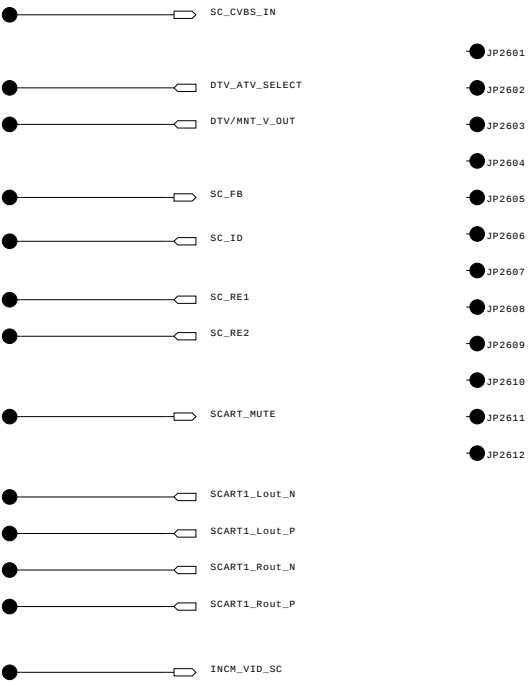


MODEL	BCM35230	DATE	
BLOCK	ATSC_TUNER	SHEET	21 / 50

Non_DVB-S2 LNB Part_Allegro



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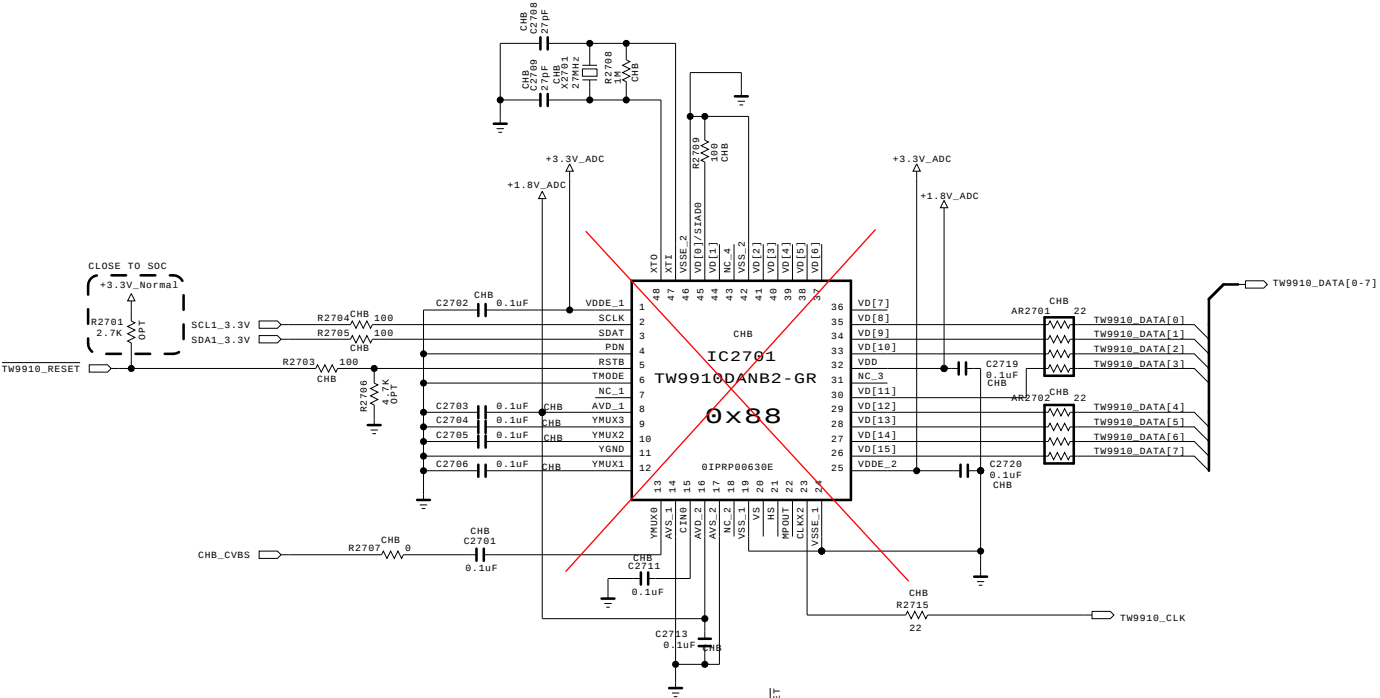
SECRET
LGElectronics



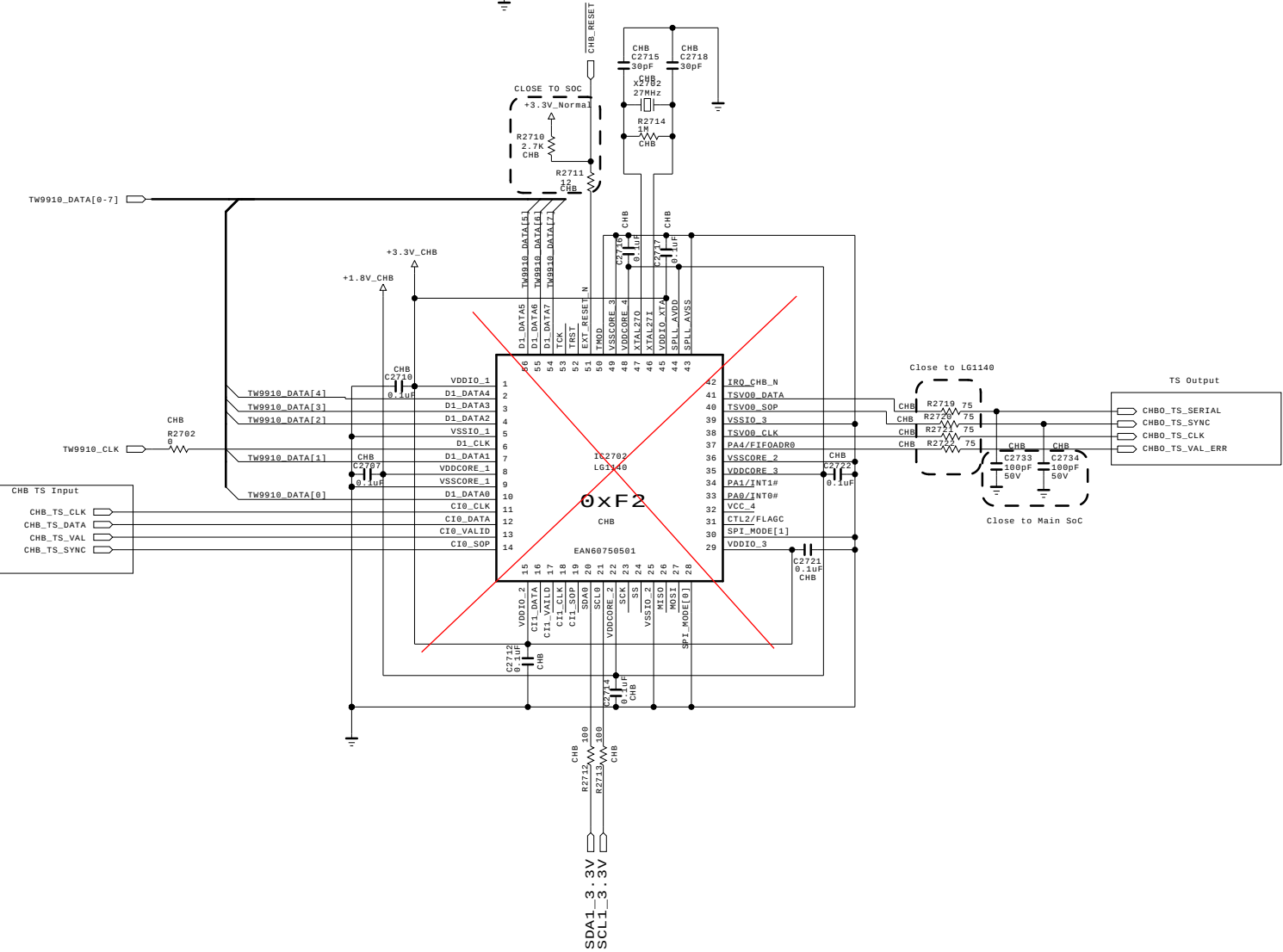
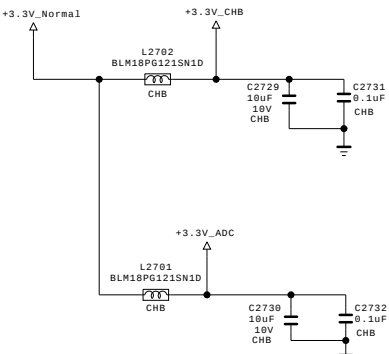
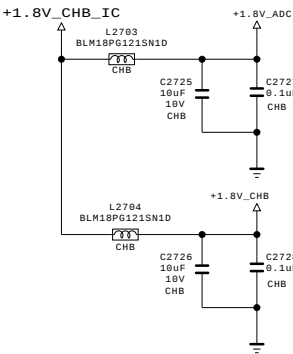
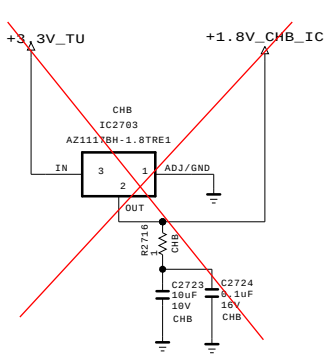
MODEL	BCM35230	DATE	
BLOCK	NON SCART	SHEET	26 / 50

CHANNEL BROWSER

DUAL COMPONENT		
IC2703	1ST : EAN61829801	2ND : EAN54428201



CHB OPTION



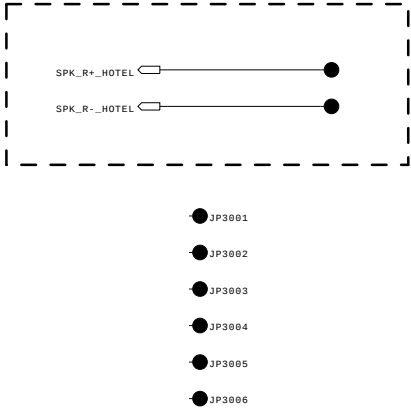
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SECRET
LGElectronics

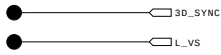


MODEL	BCM35230	DATE	
BLOCK	CHB	SHEET	27 / 50

NON CHINA HOTEL



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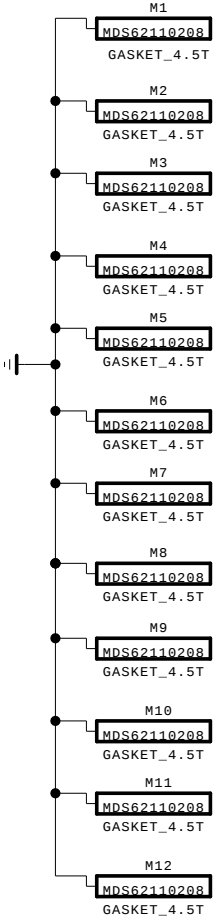
SECRET
LGElectronics



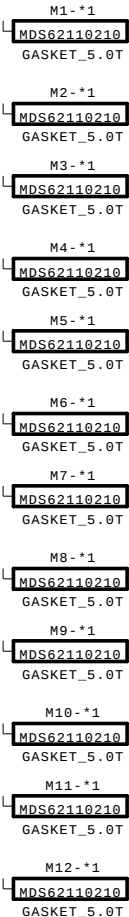
MODEL	BCM35230	DATE	
BLOCK	NON URSA	SHEET	36 / 50

SMD GASKET

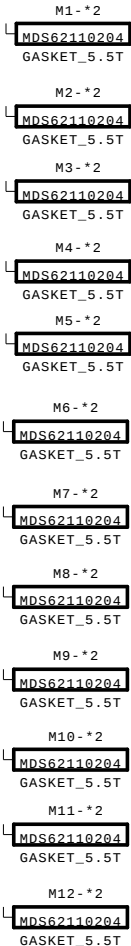
SMD GASKET 4.5T



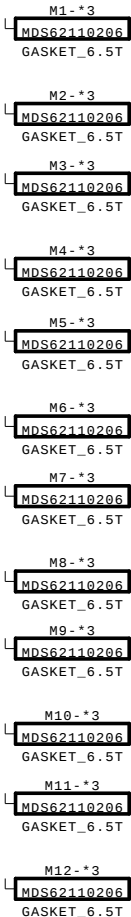
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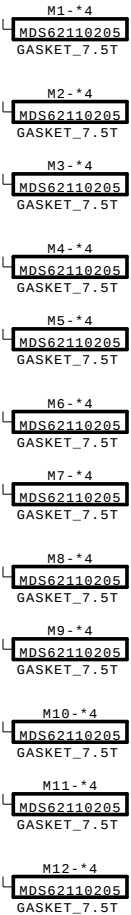
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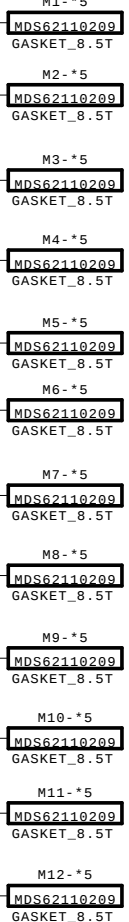
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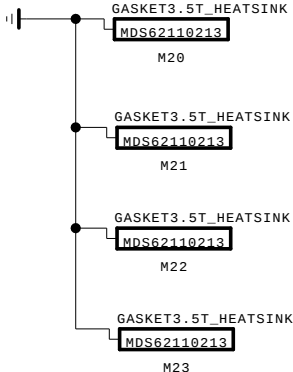
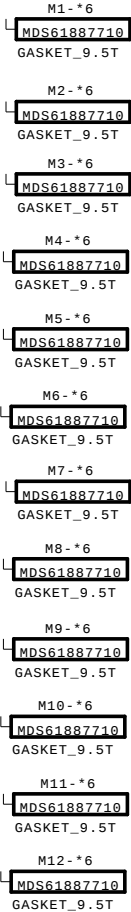
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SMD GASKET 8.5T



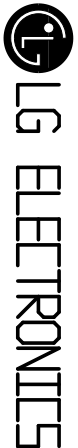
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THE  SYMBOL MARK OF THIS SCHEMETIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION, FLIRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE  SYMBOL MARK OF THE SCHEMETIC.

SECRET

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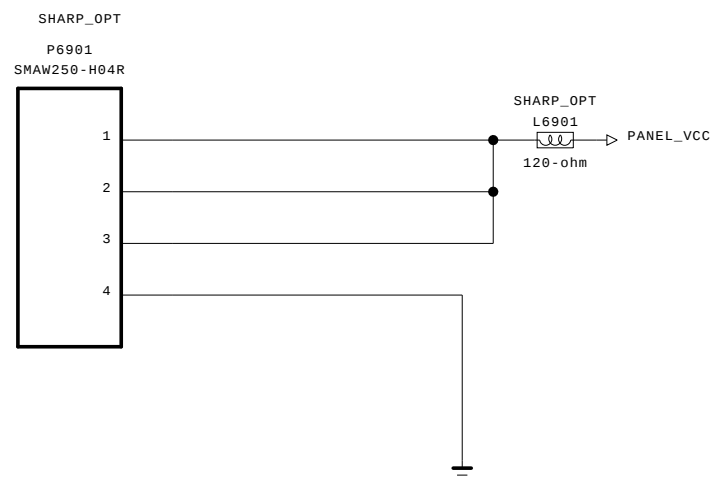
BLOCK

SMD GASKET

SHEET

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56



THE  SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE  SYMBOL MARK OF THE SCHEMATIC.

SECRET

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LG ELECTRONICS

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